VIMAL JYOTHI ENGNEERING COLLEGE

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

SUBJECT ALLOCATION FORM

Name of the faculty: Vidhya S S Academic Year: 2021-22

Designation: AP Semester: Odd/Even

Subject Preferences:

Theory

1st Choice	10CS6113	INFORMATION SECURITY (ELECTIVE I)
2nd Choice	10CS6105	COMPUTER NETWORKS AND NETWORK MANAGEMENT
3rd Choice	EST 200	DESIGN & ENGINEERING FOR COMPUTER SCIENCE
4th Choice	CST 201	DATA STRUCTURES
5th Choice	CST 203	LOGIC SYSTEM DESIGN
6th Choice	MCN 201	SUSTAINABLE ENGINEERING

Lab

1st Choice	CSL 333	DATABASE MANAGEMENT SYSTEMS LAB
2nd Choice	CSL 201	DATA STRUCTURES LAB
3rd Choice	10CS6109	SEMINAR I
4th Choice	CSL 203	OBJECT ORIENTED PROGRAMMING (IN JAVA) LAB
5th Choice	10CS6111	ADVANCED NETWORKING LAB

Dated Signature: Vidya 16/09/2020

VIMAL JYOTHI ENGINEERING COLLEGEDEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

WORK LOAD - CSE - ODD SEMESTER - AY (2021 - 2022)

Sl. No.	Name of the faculty	Subject Name	Subject Code	Slot	Class	Classroom / Lab	No. of hrs per week	Total hrs per week	Comment/any special requirement in the time table
1	Dr. JEETHU V. DEVASIA (HoD CSE)	MACHINE LEARNING (HONORS)	CS467	F	S7 CSE	Classroom	3	14	HoD
		NEURAL NETWORKS AND DEEP LEARNING (HONORS - BUCKET 2)	CST395	R/M	S5 CSE A & B	Classroom	4		
		SEMINAR & PROJECT PRELIMINARY	CS451	S	S7 CSE	Lab	7		
2	Dr. MANOJ V. THOMAS	DATA STRUCTURES	CST 201	B	S3 CSEA	Classroom	4	11	
		DATA STRUCTURES LAB	CSL 201	S	S3 CSEA	Lab	6		
		DATA STRUCTURES	CST 201	B	S3 CSEB	Tutorial	1		
3	Dr. RENJI P. CHERIAN	SEMINAR II	10CS7101	T	M3 CSE	Lab	2	6	
		PROJECT (PHASE I)	10CS7103	V	M3 CSE	Lab	4		
4	Ms. DIVYA B.	CRYPTOGRAPHY AND NETWORK SECURITY	CS409	E	S7 CSE	Classroom	3	14	
		OBJECT ORIENTED PROGRAMMING	CST 205	D	S3 CSEA	Classroom	4		
		OBJECT ORIENTED PROGRAMMING (IN JAVA) LAB	CSL 203	T	S3 CSEA	Lab	6		
		OBJECT ORIENTED PROGRAMMING	CST 205	D	S3 CSEB	Tutorial	1		
5	Ms. NEENA V. V.	OBJECT ORIENTED PROGRAMMING	CST 205	D	S3 CSEC	Classroom	4	15	
		FORMAL LANGUAGES AND AUTOMATA THEORY	CST 301	A	S5 CSE A	Classroom	4		
		OBJECT ORIENTED PROGRAMMING (IN JAVA) LAB	CSL 203	T	S3 CSEC	Lab	6		
		FORMAL LANGUAGES AND AUTOMATA THEORY	CST 301	A	S5 CSE B	Tutorial	1		
6	Ms. VIDHYA S. S.	DISTRIBUTED COMPUTING	CS407	D	S7 CSE	Classroom	3	15	
		DATA STRUCTURES	CST 201	B	S3 CSEB	Classroom	4		
		DATA STRUCTURES LAB	CSL 201	S	S3 CSEB	Lab	6		
		DATA STRUCTURES	CST 201	B	S3 CSEA	Tutorial	1		
		DATA STRUCTURES	CST 201	B	S3 CSE C	Tutorial	1		
7	Sr. JISHA C. T.	COMPUTER SYSTEM ARCHITECTURE	CS405	C	S7 CSE	Classroom	3	14	
		COMPUTER NETWORKS	CST 303	B	S5 CSE A	Classroom	4		
		DATA STRUCTURES LAB	CSL 201	S	S3 CSEB	Lab	6		
		COMPUTER NETWORKS	CST 303	B	S5 CSE B	Tutorial	1		
8	Ms. ANCY K SUNNY	LOGIC SYSTEM DESIGN	CST 203	C	S3 CSE A	Classroom	4	15	
		MICROPROCESSORS AND MICROCONTROLLERS	CST 307	D	S5 CSE B	Classroom	4		
		SYSTEM SOFTWARE AND MICROPROCESSORS LAB	CSL 331	S	S5 CSE B	Lab	6		
		MICROPROCESSORS AND MICROCONTROLLERS	CST 307	D	S5 CSE A	Tutorial	1		
9	Ms ASHA BABY	COMPUTER GRAPHICS	CS401	A	S7 CSE	Classroom	4	15	
		SYSTEM SOFTWARE	CST 305	C	S5 CSE B	Classroom	4		
		DATABASE MANAGEMENT SYSTEMS LAB	CSL 333	T	S5 CSE B	Lab	6		
		SYSTEM SOFTWARE	CST 305	C	S5 CSE A	Tutorial	1		
10	Ms. DERROLL DAVID	DATA STRUCTURES	CST 201	B	S3 CSEC	Classroom	4	17	
		DATA STRUCTURES LAB	CSL 201	S	S3 CSEC	Lab	6		
		COMPILER DESIGN LAB	CS431	T	S7 CSE	Lab	6		
		DATA STRUCTURES	CST 201	B	S3 ADS	Tutorial	1		
11	Ms. ANISHA JOSEPH	PROGRAMMING PARADIGMS	CS403	B	S7 CSE	Classroom	3	14	
		COMPUTER ALGORITHMS AND COMPLEXITY	10CS6103	B	M1 CSE	Classroom	3		
		PROJECT (PHASE I)	10CS7103	V	M3 CSE	Lab	4		
		SEMINAR II	10CS7101	T	M3 CSE	Lab	2		
		SEMINAR I	10CS6109	T	M1 CSE	Lab	2		
12	Ms. AKHILA MATHEW	DATA STRUCTURES	CST 201	B	S3 ADS	Classroom	4	14	
		INFORMATION SECURITY (ELECTIVE I)	10CS6113	E	M1 CSE	Classroom	3		
		ADVANCED NETWORKING LAB	10CS6111	U	M1 CSE	Lab	4		
		DATA STRUCTURES LAB	CSL 201	S	S3 ADS	Lab	3		
13	Ms. KEERTHIJITH P.	COMPUTER NETWORKS AND NETWORK MANAGEMENT	10CS6105	C	M1 CSE	Classroom	3	15	
		COMPUTER NETWORKS	CST 303	B	S5 CSE B	Classroom	4		
		COMPILER DESIGN LAB	CS431	T	S7 CSE	Lab	6		
		COMPUTER NETWORKS	CST 303	B	S5 CSE A	Tutorial	1		
		LOGIC SYSTEM DESIGN	CST 203	C	S3 ADS	Tutorial	1		
14	Ms. ACHALA PRASAD	MICROPROCESSORS AND MICROCONTROLLERS	CST 307	D	S5 CSE A	Classroom	4	18	
		SYSTEM SOFTWARE AND MICROPROCESSORS LAB	CSL 331	S	S5 CSE A	Lab	6		
		MICROPROCESSORS AND MICROCONTROLLERS	CST 307	D	S5 CSE B	Tutorial	1		
		SEMINAR & PROJECT PRELIMINARY	CS451	S	S7 CSE	Lab	7		
15	Ms. TINTU DEVASIA	OBJECT ORIENTED PROGRAMMING (MINOR - BUCKET 1)	CST281	R/M	S3 Semester	Classroom	4	15	
		OBJECT ORIENTED PROGRAMMING	CST 205	D	S3 CSEB	Classroom	4		
		OBJECT ORIENTED PROGRAMMING (IN JAVA) LAB	CSL 203	T	S3 CSEB	Lab	6		
		OBJECT ORIENTED PROGRAMMING	CST 205	D	S3 CSEA	Tutorial	1		
16	Ms. DIVYA K.	LOGIC SYSTEM DESIGN	CST 203	C	S3 CSE B	Classroom	4	15	
		IT TOOLS FOR MANAGERS	MBA				4		
		SYSTEM SOFTWARE AND MICROPROCESSORS LAB	CSL 331	S	S5 CSE B	Lab	6		
		LOGIC SYSTEM DESIGN	CST 203	C	S3 CSE C	Tutorial			
17	Mr. ABDUL LATHEEF	LOGIC SYSTEM DESIGN	CST 203	C	S3 CSE C	Classroom	4	16	
		LOGIC SYSTEM DESIGN	CST 203	C	S3 ADS	Classroom	4		
		SYSTEM SOFTWARE AND MICROPROCESSORS LAB	CSL 331	S	S5 CSE A	Lab	6		
		LOGIC SYSTEM DESIGN	CST 203	C	S3 CSE A	Tutorial	1		
		LOGIC SYSTEM DESIGN	CST 203	C	S3 CSE B	Tutorial	1		
18	Ms. ANGEL VARGHESE	MANAGEMENT OF SOFTWARE SYSTEMS	CST 309	E	S5 CSE B	Classroom	4	15	
		SOFTWARE PROJECT MANAGEMENT	10CS7111	A	M3 CSE	Classroom	3		
		CONCEPTS IN SOFTWARE ENGINEERING (MINOR - BUCKET 1)	CST381	R/M	S5 ECE S5 EEE S5 CE A&B S5 ME B	Tutorial	1		
		DATABASE MANAGEMENT SYSTEMS LAB	CSL 333	T	S5 CSE B	Lab	6		
		CONCEPTS IN MACHINE LEARNING (MINOR - BUCKET 2)	CST383	R/M	S5 AEI & S5 EEE	Tutorial	1		
19	Ms. AMBILI M.A	BIOINFORMATICS (ELECTIVE III)	CS465	F	S7 CSE	Classroom	3	15	
		CONCEPTS IN SOFTWARE ENGINEERING (MINOR - BUCKET 1)	CST381	R/M	S5 ECE S5 EEE	Classroom	4		
		DATABASE MANAGEMENT SYSTEMS LAB	CSL 333	T	S5 CSE A	Lab	6		
		OBJECT ORIENTED PROGRAMMING	CST 205	D	S3 CSE C	Tutorial	1		
		OBJECT ORIENTED PROGRAMMING	CST 205	D	S3 ADS	Tutorial	1		
20	Ms. NAYANA SURESH	FORMAL LANGUAGES AND AUTOMATA THEORY	CST 301	A	S5 CSE B	Classroom	4	13	
		OBJECT ORIENTED PROGRAMMING	CST 205	D	S3 ADS	Classroom	4		
		OBJECT ORIENTED PROGRAMMING (IN JAVA) LAB	CSL 203	T	S3 ADS	Lab	3		
		NEURAL NETWORKS AND DEEP LEARNING (HONORS - BUCKET 2)	CST395	R/M	S5 CSE A & B	Tutorial	1		
		FORMAL LANGUAGES AND AUTOMATA THEORY	CST 301	A	S5 CSE A	Tutorial	1		
21	CS1	SYSTEM SOFTWARE	CST 305	C	S5 CSE A	Classroom	4	15	
		CONCEPTS IN MACHINE LEARNING (MINOR - BUCKET 2)	CST383	R/M	S5 AEI & S5 EEE	Classroom	4		
		DATABASE MANAGEMENT SYSTEMS LAB	CSL 333	T	S5 CSE A	Lab	6		
		SYSTEM SOFTWARE	CST 305	C	S5 CSE B	Tutorial	1		
22	CS2	SUSTAINABLE ENGINEERING	MCN 201	F	S3 CSE A	Classroom	2	14	
		SUSTAINABLE ENGINEERING	MCN 201	F	S3 CSE B	Classroom	2		
		DATA STRUCTURES LAB	CSL 201	S	S3 CSEA	Lab	6		
		OBJECT ORIENTED PROGRAMMING (MINOR - BUCKET 1)	CST281	R/M	S3 Semester	Tutorial	1		
		MANAGEMENT OF SOFTWARE SYSTEMS	CST 309	E	S5 CSE A	Classroom	3		
23	CS3	DATA STRUCTURES LAB	CSL 201	S	S3 CSEC	Lab	6	14	
		COMPUTER ARCHITECTURE	10CS6107	D	M1 CSE	Classroom	3		
		DESIGN & ENGINEERING FOR COMPUTER SCIENCE	EST 200	E	S3 CSE A	Classroom	2		
		DESIGN & ENGINEERING FOR COMPUTER SCIENCE	EST 200	E	S3 CSE B	Classroom	2		
		OBJECT ORIENTED PROGRAMMING (MINOR - BUCKET 1)	CST281	R/M	S3 Semester	Tutorial	1		
24	CS4	OBJECT ORIENTED PROGRAMMING (MINOR - BUCKET 1)	CST281	R/M	S3 Semester	Classroom	4	14	
		OBJECT ORIENTED PROGRAMMING (IN JAVA) LAB	CSL 203	T	S3 CSEC	Lab	6		
		DISASTER MANAGEMENT	MCN 301	F	S5 CSE B	Classroom	2		
		DISASTER MANAGEMENT	MCN 301	F	S5 CSE A	Classroom	2		
25	CS5	SUSTAINABLE ENGINEERING	MCN 201	F	S3 CSE C	Classroom	2	16	
		SUSTAINABLE ENGINEERING	MCN 201	F	S3 ADS	Classroom	2		
		OBJECT ORIENTED PROGRAMMING (IN JAVA) LAB	CSL 203	T	S3 CSEA	Lab	6		
		OBJECT ORIENTED PROGRAMMING (IN JAVA) LAB	CSL 203	T	S3 CSEB	Lab	6		
26	CS6	DESIGN & ENGINEERING FOR COMPUTER SCIENCE	EST 200	E	S3 ADS	Classroom	2	13	
		DESIGN & ENGINEERING FOR COMPUTER SCIENCE	EST 200	E	S3 CSE C	Classroom	2		
		DATA STRUCTURES LAB	CSL 201	S	S3 ADS	Lab	3		
		OBJECT ORIENTED PROGRAMMING (IN JAVA) LAB	CSL 203	T	S3 ADS	Lab	3		
		INFORMATION STORAGE MANAGEMENT	10CS7129	B	M3 CSE	Classroom	3		

APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY
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CIRCULAR

Sub:

Academic Calendar of Various Even Semesters Programmes - Published - Circulated - Reg.

The Academic Calendar of even semesters (March 2022 to August 2022) for **B.Tech S8, BHMCT S6, B.Des S6/S4/S2, MBA S2, MCA/Int MCA S2 programmes** is published herewith.

Dr. Sadiq A.
Dean (Academic)

To:

1. Principals of all affiliated Institutions.
2. Controller of Examinations
3. KTU Support.
4. AD-IT (for publishing in the website).

* This is a computer system (Digital File) generated letter. Hence there is no need for a physical signature.

APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY

Academic Calendar - March 2022 to August 2022

B.Tech S8, BHMCT S6,B.Des S6/S4/S2,MBA S2, MCA/Int MCA S2

Mar-22				Apr-22				May-22			
Days	Date	Description	Class	Days	Date	Description	Class	Days	Date	Description	Class
Tue	1	Maha Sivarathri		Fri	1		26	Sun	1	May Day	
Wed	2	Commencement of classes MCA/Int MCA S2	1	Sat	2		27	Mon	2	Id-ul-Fitr	
Thu	3	Commencement of classes B.Tech S8, BHMCT S6,B.Des S6	2	Sun	3			Tue	3		49
Fri	4		3	Mon	4	Course Selection and Mapping Begin B.Des S4/S2	28	Wed	4		50
Sat	5		4	Tue	5		29	Thu	5		51
Sun	6			Wed	6	Course Selection and Mapping Ends B.Des S4/S2	30	Fri	6		52
Mon	7		5	Thu	7		31	Sat	7		53
Tue	8		6	Fri	8	First CC Meeting B.Des S4/S2	32	Sun	8		
Wed	9	Commencement of classes MBA S2	7	Sat	9			Mon	9		54
Thu	10		8	Sun	10			Tue	10		55
Fri	11		9	Mon	11	First Series Test B.Tech S8, BHMCT S6,B.Des S6,MBA S2, MCA/Int MCA S2	33	Wed	11		56
Sat	12			Tue	12		34	Thu	12		57
Sun	13			Wed	13		35	Fri	13		58
Mon	14	First CC Meeting B.Tech S8, BHMCT S6,B.Des S6,MBA S2, MCA/Int MCA S2	10	Thu	14	Maundy Thursday		Sat	14		
Tue	15		11	Fri	15	Vishu/ Good Friday		Sun	15		
Wed	16	Course Selection and Mapping Begins B.Tech S8, BHMCT S6,B.Des S6,MBA S2, MCA/Int MCA S2	12	Sat	16		36	Mon	16		59
Thu	17		13	Sun	17			Tue	17	Exam Registration for B.Tech S8, BHMCT S6,B.Des S6/S4/S2,MBA S2, MCA/Int MCA S2	60
Fri	18	Course Selection and Mapping Ends B.Tech S8, BHMCT S6,B.Des S6,MBA S2, MCA/Int MCA S2	14	Mon	18		37	Wed	18		61
Sat	19		15	Tue	19		38	Thu	19	First Series Test B.Des S4/S2	62
Sun	20			Wed	20		39	Fri	20		63
Mon	21		16	Thu	21		40	Sat	21	Exam Registration Ends for B.Tech S8, BHMCT S6,B.Des S6/S4/S2,MBA S2, MCA/Int MCA S2	64
Tue	22		17	Fri	22		41	Sun	22		
Wed	23		18	Sat	23		42	Mon	23		65
Thu	24		19	Sun	24			Tue	24		66
Fri	25		20	Mon	25		43	Wed	25	Second CC Meeting B.Des S4/S2	67
Sat	26		21	Tue	26		44	Thu	26	Second Series Test B.Tech S8, BHMCT S6,B.Des S6,MBA S2, MCA/Int MCA S2	68
Sun	27			Wed	27		45	Fri	27		69
Mon	28	Commencement of classes B.Des S4/S2	22	Thu	28	Second CC Meeting B.Tech S8, BHMCT S6,B.Des S6,MBA S2, MCA/Int MCA S2	46	Sat	28		70
Tue	29		23	Fri	29		47	Sun	29		
Wed	30		24	Sat	30		48	Mon	30		71
hu	31		25					Tue	31		72

APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY

Academic Calendar - March 2022 to August 2022

B.Tech S8, BHMCT S6,B.Des S6/S4/S2,MBA S2, MCA/Int MCA S2

Jun-22				Jul-22				Aug-22			
Days	Date	Description	Class	Days	Date	Description	Class	Days	Date	Description	Class
Wed	1	Third CC Meeting B.Tech S8, BHMCT S6,B.Des S6,MBA S2, MCA/Int MCA S2	73	Fri	1			Mon	1		
Thu	2		74	Sat	2			Tue	2		
Fri	3	Publish IA Marks for B.Tech S8 ,B.Des S6	75	Sun	3			Wed	3		
Sat	4	Class Ends Publish Attendance for B.Tech S8 ,B.Des S6	76	Mon	4			Thu	4		
Sun	5			Tue	5			Fri	5		
Mon	6		77	Wed	6			Sat	6		
Tue	7		78	Thu	7			Sun	7		
Wed	8		79	Fri	8			Mon	8	Muharram	
Thu	9		80	Sat	9			Tue	9		
Fri	10		81	Sun	10			Wed	10		
Sat	11			Mon	11	Commencement of End Semester Examination BHMCT S6/B.Des S2/B.Des S4		Thu	11		
Sun	12			Tue	12			Fri	12		
Mon	13		82	Wed	13			Sat	13		
Tue	14		83	Thu	14			Sun	14		
Wed	15	Second Series Test B.Des S4/S2 Publish IA Marks for MBA S2, MCA/Int MCA S2 Commencement of End Semester Examination B.Tech S8/B.Des S6	84	Fri	15			Mon	15	Independence Day	
Thu	16	Class Ends Publish Attendance for MBA S2, MCA/Int MCA S2	85	Sat	16			Tue	16		
Fri	17		86	Sun	17			Wed	17		
Sat	18		87	Mon	18			Thu	18	Sreekrishna Jayanthi	
Sun	19			Tue	19			Fri	19		
Mon	20		88	Wed	20			Sat	20		
Tue	21		89	Thu	21			Sun	21		
Wed	22		90	Fri	22			Mon	22		
Thu	23	Third CC Meeting B.Des S4/S2	91	Sat	23			Tue	23		
Fri	24	Publish IA Marks for BHMCT S6	92	Sun	24			Wed	24		
Sat	25	Class Ends Publish Attendance for BHMCT S6	93	Mon	25			Thu	25		
Sun	26			Tue	26			Fri	26		
Mon	27	Commencement of End Semester Examination MBA S2, MCA/Int MCA S2	94	Wed	27			Sat	27		
Tue	28		95	Thu	28	Karkadaka Vavu		Sun	28		
Wed	29	Publish IA Marks for B.Des S4/S2	96	Fri	29			Mon	29		
Thu	30	Class Ends Publish Attendance for B.Des S4/S2	97	Sat	30			Tue	30		
				Sun	31			Wed	31		

APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY

Academic Calendar - March 2022 to August 2022

B.Tech S8, BHMCT S6,B.Des S6/S4/S2,MBA S2, MCA/Int MCA S2

Events		
Sl.No	**Even Semester(2021-22)**	**Important Dates**
1	Commencement of classes MCA/Int MCA S2	02-03-2022
2	Commencement of classes B.Tech S8, BHMCT S6,B.Des S6	03-03-2022
3	Commencement of classes MBA S2	09-03-2022
4	Commencement of classes B.Des S4/S2	28-03-2022
5	CC Meetings for B.Tech S8, BHMCT S6,B.Des S6,MBA S2, MCA/Int MCA S2	14-03-2022, 28-04-2022, 01-06-2022
6	CC Meetings for B.Des S4/S2	08-04-2022, 25-05-2022, 23-06-2022
7	Course Selection and Mapping Begins B.Tech S8, BHMCT S6,B.Des S6,MBA S2, MCA/Int MCA S2	16-03-2022
8	Course Selection and Mapping Begins B.Des S4/S2	04-04-2022
9	Course Selection and Mapping Ends B.Tech S8, BHMCT S6,B.Des S6,MBA S2, MCA/Int MCA S2	18-03-2022
10	Course Selection and Mapping Ends B.Des S4/S2	06-04-2022
11	Commencement of First Series Test B.Tech S8, BHMCT S6,B.Des S6,MBA S2, MCA/Int MCA S2	11-04-2022
12	Commencement of First Series Test B.Des S4/S2	19-05-2022
13	First Series test to be completed B.Tech S8, BHMCT S6,B.Des S6,MBA S2, MCA/Int MCA S2	13-04-2022
14	First Series to be completed Test B.Des S4/S2	21-05-2022
15	Commencement of Second Series B.Tech S8, BHMCT S6,B.Des S6,MBA S2, MCA/Int MCA S2	26-05-2022
16	Commencement of Second Series test B.Des S4/S2	15-06-2022
17	Second Series test to be completed for B.Tech S8, BHMCT S6,B.Des S6,MBA S2, MCA/Int MCA S2	28-05-2022
18	Second Series test to be completed for B.Des S4/S2	17-06-2022
19	Exam Registration begins for B.Tech S8, BHMCT S6,B.Des S6/S4/S2,MBA S2, MCA/Int MCA S2	17-05-2022
20	Exam Registration Ends for B.Tech S8, BHMCT S6,B.Des S6/S4/S2,MBA S2, MCA/Int MCA S2	21-05-2022
21	Publish IA Marks for B.Tech S8 ,B.Des S6	03-06-2022
22	Class Ends Publish Attendance for B.Tech S8 ,B.Des S6	04-06-2022
23	Publish IA Marks for MBA S2, MCA/Int MCA S2	15-06-2022
24	Class Ends Publish Attendance for MBA S2, MCA/Int MCA S2	16-06-2022
25	Publish IA Marks for BHMCT S6	24-06-2022
26	Class Ends Publish Attendance for BHMCT S6	25-06-2022
27	Publish IA Marks for B.Des S4/S2	29-06-2022
28	Class Ends Publish Attendance for B.Des S4/S2	30-06-2022
29	Commencement of End Semester Examination B.Tech S8/B.Des S6	15-06-2022
30	Commencement of End Semester Examination MBA S2, MCA/Int MCA S2	27-06-2022
31	Commencement of End Semester Examination BHMCT S6/B.Des S2/B.Des S4	11-07-2022

ACADEMIC CALENDAR - 2021-22 ODD SEMESTER

VIMAL JYOTHI ENGINEERING COLLEGE, CHEMPERI

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

SEPTEMBER-2021

DAYS	DATES	ACADEMIC ACTIVITIES
Wed	1	
Thu	2	
Fri	3	
Sat	4	
Sun	5	
Mon	6	
Tue	7	
Wed	8	
Thu	9	
Fri	10	ACM Student chapter workshop in association with Outscal on " Game Development Masters Class"
Sat	11	
Sun	12	
Mon	13	
Tue	14	
Wed	15	
Thu	16	
Fri	17	
Sat	18	ACM Student chapter & CSI event one day workshop on cyber safety by SISTMR Australia
Sun	19	
Mon	20	
Tue	21	Sree Narayana Guru Samadhi
Wed	22	
Thu	23	
Fri	24	
Sat	25	
Sun	26	
Mon	27	Commencement of S7
Tue	28	
Wed	29	
Thu	30	

ACADEMIC CALENDAR - 2021-22 ODD SEMESTER

VIMAL JYOTHI ENGINEERING COLLEGE, CHEMPERI

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

OCTOBER-2021

DAYS	DATES	ACADEMIC ACTIVITIES
Fri	1	
Sat	2	Gandhi Jayanthi
Sun	3	
Mon	4	
Tue	5	IA publish - S2, S4 and M2
Wed	6	
Thu	7	S7 1st class committee to be completed
Fri	8	S2 Class ends
Sat	9	
Sun	10	
Mon	11	S7 Offline class
Tue	12	
Wed	13	S4 and M2 Class Ends
Thu	14	Maha navami
Fri	15	Vijayadasami
Sat	16	
Sun	17	
Mon	18	
Tue	19	Nabi Dinam
Wed	20	S2 ESE
Thu	21	
Fri	22	
Sat	23	
Sun	24	
Mon	25	
Tue	26	
Wed	27	
Thu	28	
Fri	29	
Sat	30	
Sun	31	

ACADEMIC CALENDAR - 2021-22 ODD SEMESTER

VIMAL JYOTHI ENGINEERING COLLEGE, CHEMPERI

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

NOVEMBER - 2021

DAYS	DATES	ACADEMIC ACTIVITIES
Mon	1	S7 - Internal exam 1
Tue	2	S7 - Internal exam 1
Wed	3	S7 - Internal exam 1
Thu	4	Deepavali
Fri	5	
Sat	6	
Sun	7	
Mon	8	
Tue	9	
Wed	10	
Thu	11	
Fri	12	S7 Assignment 1 to be completed
Sat	13	
Sun	14	
Mon	15	Commencement of S3 BTech and M3 classes
Tue	16	
Wed	17	RA S7 IA1
Thu	18	Commencement of S5 classes
Fri	19	
Sat	20	S7 project preliminary - first review to be completed
Sun	21	
Mon	22	Commencement of S1 BTech
Tue	23	Publish S7 Assignment 1 mark
Wed	24	
Thu	25	
Fri	26	
Sat	27	Induction program Ends ACM Student chapter workshop on " Road map to SRE/DevOps"
Sun	28	
Mon	29	Commencement of S1 MTech
Tue	30	

ACADEMIC CALENDAR - 2021-22 ODD SEMESTER

VIMAL JYOTHI ENGINEERING COLLEGE, CHEMPERI

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

DECEMBER-2021

DAYS	DATES	ACADEMIC ACTIVITIES
Wed	1	First Advisory meeting of S3 ,S5 and M3 to be completed
Thu	2	
Fri	3	
Sat	4	
Sun	5	
Mon	6	S7 - Internal exam 2
Tue	7	S7 - Internal exam 2
Wed	8	S7 - Internal exam 2
Thu	9	
Fri	10	First Advisory meeting of S1
Sat	11	
Sun	12	
Mon	13	
Tue	14	
Wed	15	
Thu	16	
Fri	17	S7 project preliminary - second review to be completed
Sat	18	Workshop on "Introduction & object orientation in PHP" for S7CSE students.
Sun	19	
Mon	20	
Tue	21	S3, S5, M3 Internal exam 1
Wed	22	
Thu	23	
Fri	24	CHRISTMAS VACATION
Sat	25	
Sun	26	
Mon	27	
Tue	28	
Wed	29	
Thu	30	
Fri	31	

ACADEMIC CALENDAR - 2021-22 ODD SEMESTER

VIMAL JYOTHI ENGINEERING COLLEGE, CHEMPERI

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

JANUARY - 2022

DAYS	DATES	ACADEMIC ACTIVITIES
Sat	1	
Sun	2	
Mon	3	S3 & S5 Assignment 1 to be completed S7 Resit exam
Tue	4	S7 Resit exam S7 IA2 RA
Wed	5	S7 Resit exam
Thu	6	Publish S7 - IA mark
Fri	7	S1 BTech and MTech - Internal exam 1
Sat	8	
Sun	9	
Mon	10	S1 BTech and MTech - Internal exam 1
Tue	11	S1 BTech and MTech - Internal exam 1
Wed	12	S7 class ends and publish attendance
Thu	13	
Fri	14	
Sat	15	
Sun	16	
Mon	17	RA - S3 ,S5 and M3 IA1
Tue	18	
Wed	19	
Thu	20	
Fri	21	
Sat	22	TECHNIQ - Quiz for S1& S3 CSE students
Sun	23	
Mon	24	S7 ESE Starts
Tue	25	
Wed	26	Republic Day
Thu	27	
Fri	28	Second Advisory meeting of S1, S3, S5 and M3 to be completed
Sat	29	ACM Student chapter quiz contest for S1 & S3 CSE students.
Sun	30	
Mon	31	

ACADEMIC CALENDAR - 2021-22 ODD SEMESTER

VIMAL JYOTHI ENGINEERING COLLEGE, CHEMPERI

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

FEBRUARY-2022

DAYS	DATES	ACADEMIC ACTIVITIES
Tue	1	
Wed	2	Publish S3 &S5 Assignment 1 mark
Thu	3	
Fri	4	
Sat	5	S3, S5, M1 & M3 - Internal exam 2
Sun	6	
Mon	7	S3,S5,M1 & M3 - Internal exam 2
Tue	8	S3, S5, M1 & M3 - Internal exam 2
Wed	9	Publish S1 Assignment 1 mark Workshop on real world applications of microcontrollers for S5 CSE students
Thu	10	
Fri	11	S1 - Internal exam 2
Sat	12	
Sun	13	
Mon	14	S1 - Internal exam 2
Tue	15	S1 - Internal exam 2
Wed	16	S3 & S5 Internal Lab exam to be completed
Thu	17	S3 & S5 Assignment 2 to be completed
Fri	18	
Sat	19	
Sun	20	
Mon	21	RA- S3 , S5 & M3 IA2
Tue	22	
Wed	23	Third advisory meeting of S3, S5 and M3 to be completed
Thu	24	S7 ESE ENDS Workshop on Online simulation of digital circuits for S3 CSE/ADS
Fri	25	Publish IA mark of S3 ,S5 and M3 RA - S1 IA2 DS gap filling activity for S3 CSE students
Sat	26	
Sun	27	
Mon	28	Commencement of S8 Class S3 & S5 Class ends and publish Attendance Java gap filling activity for S3 CSE Students

ACADEMIC CALENDAR - 2021-22 ODD SEMESTER

VIMAL JYOTHI ENGINEERING COLLEGE, CHEMPERI

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

MARCH-2022

DAYS	DATES	ACADEMIC ACTIVITIES
Tue	1	Sivarathri
Wed	2	S1 Internal Lab exam to be completed
Thu	3	Third Advisory meeting of S1 BTech and Mtech to be completed
Fri	4	
Sat	5	
Sun	6	
Mon	7	Publish IA mark of S1 BTech and Mtech
Tue	8	
Wed	9	S1 BTech and MTech Class ends and publish attendance
Thu	10	
Fri	11	
Sat	12	
Sun	13	
Mon	14	
Tue	15	
Wed	16	
Thu	17	
Fri	18	
Sat	19	
Sun	20	
Mon	21	
Tue	22	
Wed	23	
Thu	24	
Fri	25	
Sat	26	
Sun	27	
Mon	28	
Tue	29	
Wed	30	
Thu	31	

VIMAL JYOTHI ENGINEERING COLLEGE, CHEMPERI
DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING
THIRD SEMESTER BTECH CSE C BATCH (2020 - 2024)
ASSESSMENT PLAN

Slot	Code	Subjects	Faculty	Assignment	Issue Date	Submission Date	Return Date
A	MAT 203	DISCRETE MATHEMATICAL STRUCTURES	MS. ANJU SUNNY	1	28/12/202	6/1/2022	11/01/2022
				2	10/2/2022	22/2/2022	03/03/2022
B	CST201	DATA STRUCTURES	Ms. DERROLL DAVID	1	26/11/2021	09/12/2021	16/12/2021
				2	14/02/2022	23/02/2022	27/02/2022
C	CST203	LOGIC SYSTEM DESIGN	Mr. ABDUL LATHEEF	1	29/11/21	7/12/21	15/12/21
				2	27/1/22	3/2/22	11/2/22
D	CST205	OBJECT ORIENTED PROGRAMMING	Ms. NEENA V. V.	1	15/12/2021	28/12/2021	7/1/2022
				2	15/2/2022	25/2/2022	8/3/2022
E	EST200	DESIGN AND ENGINEERING FOR COMPUTER SCIENCE	Ms. NAMITHA P./ Ms. ANIT THOMAS	1	13/12/2021	23/12/2021	31/12/2021
				2	2/2/2022	11/2/2022	21/02/2022
F	MCN201	SUSTAINABLE ENGINEERING	Mr. ALEX GEORGE	1	10/12/2021	17/12./2021	27/12/2021
				2	17/01/2022	27/01/2022	10/02/2022
	ECT281	ELECTRONICS CIRCUITS - MINOR COURSE	Ms.LEKSHMY S	1	17/12/2021	30/12/21	7/1/2022
				2	04/02/22	18/02/22	25/02/22

Faculty Advisors

Mr. Abdul Latheef

Ms. Angel Varghese

HoD- CSE

Dr. Jeethu V. Devasia

VIMAL JYOTHI ENGINEERING COLLEGE, CHEMPERI

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

THIRD SEMETER B.TECH (2020-'24 C BATCH)

TARGET PASS %

Code	Subjects	Faculty	Pass %			Average Pass %	Target Pass%
			CAY-3	CAY-2	CAY-1		
CST201	Data Structures	Ms. DERROLL DAVID	91.38	85	91.53	89.3	94.65
CST203	Logic System Design	Mr. ABDUL LATHEEF	81.03	78.33	90.68	83.35	91.675
CST205	Object Oriented Programming Using Java	Ms. NEENA V. V.	89.66	NA	84.75	87.21	93.605
MAT203	Discrete Mathematical Structures	MS. RAIZA YOUSEF	93.1	86.67	82.2	87.32	93.66
EST200	Design & Engineering	CS2	100	100	100	100	100
MCN201	Sustainable Engineering	Mr. ALEX GEORGE	100	100	100	100	100
CSL201	Data Structures Lab	Ms. DERROLL DAVID, Ms. ANISHA JOSEPH	100	100	100	100	100
CSL203	Object Oriented Programming Lab(in Java)	Ms. NEENA V.V, Sr. JISHA C.T	100	100	100	100	100

Faculty Advisors

Mr. Abdul Latheef 

Ms. Angel Varghese

HoD

Dr. Jeethu V Devasia

AY 2021-22 S2 CSE A

Vimal Jyothi Engineering College, Chemperi, Kannur Dist., Kerala

	1	2	3	4	5	6
Mo	PHT100 ENGINEERING PHYSICS *Jomy Jose*	EST 110 ENGINEERING GRAPHICS *Mr. Midhun Mukundan*	MAT 102 VECTOR CALCULUS, DIFFERENTIAL EQUATIONS AND TRANSFORMS *Siji P*	EST 130 BASICS OF ELECTRICAL ENGINEERING *Mr.Jijo Joseph*	**EST 110 ENGINEERING GRAPHICS PRACTICAL** *Mr. Midhun Mukundan / Ms.VINAYA S M*	
Tu	PHT100 ENGINEERING PHYSICS *Jomy Jose*	**HUT 102 PROFESSIONAL COMMUNICATION PRACTICAL** *Ms.Grace John M*		EST 130 BASICS OF ELECTRONICS ENGINEERING *Mr.Adarsh K S*	**EST 102 PROGRAMMING IN C PRACTICAL** *Ms. DERROLL DAVID / Ms. TINTU DEVASIA*	
We	EST 102 PROGRAMMING IN C TUT *Ms. DERROLL DAVID*	**ESL 130 ELECTRICAL WORKSHOP/ ESL 130 ELECTRONICS WORKSHOP** *Mr.Jijo Joseph / F / L / Ms.Sudharsana Vijayan*		EST 130 BASICS OF ELECTRONICS ENGINEERING *Mr.Adarsh K S*	PHT100 ENGINEERING PHYSICS *Jomy Jose*	MAT 102 VECTOR CALCULUS, DIFFERENTIAL EQUATIONS AND TRANSFORMS *Siji P*
Th	HUT 102 PROFESSIONAL COMMUNICATION *Ms.Grace John M*	EST 102 PROGRAMMING IN C *Ms. DERROLL DAVID*	PHT100 ENGINEERING PHYSICS TUT *Jomy Jose*	EST 110 ENGINEERING GRAPHICS *Mr. Midhun Mukundan*	**PHL120 ENGINEERING PHYSICS LAB/ Library** *Jomy Jose / Mr. Stantly Kurian*	
Fr	MAT 102 VECTOR CALCULUS, DIFFERENTIAL EQUATIONS AND TRANSFORMS TUT *Siji P*	MAT 102 VECTOR CALCULUS, DIFFERENTIAL EQUATIONS AND TRANSFORMS *Prof. George K V*	EST 130 BASICS OF ELECTRICAL ENGINEERING *Mr.Jijo Joseph*	EST 102 PROGRAMMING IN C *Ms. DERROLL DAVID*	MAT 102 VECTOR CALCULUS, DIFFERENTIAL EQUATIONS AND TRANSFORMS *Siji P*	HUT 102 PROFESSIONAL COMMUNICATION *Ms.Grace John M*
Sa	**Special Timetable**					

AY 2021-22 S2 CSE B

Vimal Jyothi Engineering College, Chemperi, Kannur Dist., Kerala

	1	2	3	4	5	6
Mo	PHT100 ENGINEERING PHYSICS *Soumya John*	MAT 102 VECTOR CALCULUS, DIFFERENTIAL EQUATIONS AND TRANSFORMS *Prof. George K V*	HUT 102 PROFESSIONAL COMMUNICATION *Ms.Ann Mathew*	EST 102 PROGRAMMING IN C *Ms. DIVYA B.*	**PHL120 ENGINEERING PHYSICS LAB/ Library** *Soumya John / Mr. Stantly Kurian*	
Tu	EST 110 ENGINEERING GRAPHICS *Dr.Sreekanth M P*	**HUT 102 PROFESSIONAL COMMUNICATION PRACTICAL** *Ms.Ann Mathew*		EST 130 BASICS OF ELECTRICAL ENGINEERING *Mr.Prabin James*	EST 102 PROGRAMMING IN C *Ms. DIVYA B.*	MAT 102 VECTOR CALCULUS, DIFFERENTIAL EQUATIONS AND TRANSFORMS *Prof. George K V*
We	PHT100 ENGINEERING PHYSICS *Soumya John*	MAT 102 VECTOR CALCULUS, DIFFERENTIAL EQUATIONS AND TRANSFORMS *Prof. George K V*	EST 130 BASICS OF ELECTRICAL ENGINEERING *Mr.Prabin James*	PHT100 ENGINEERING PHYSICS *Soumya John*	**EST 110 ENGINEERING GRAPHICS PRACTICAL** *Ms.VINAYA S M / Dr.Sreekanth M P*	
Th	EST 102 PROGRAMMING IN C TUT *Ms. DIVYA B.*	MAT 102 VECTOR CALCULUS, DIFFERENTIAL EQUATIONS AND TRANSFORMS TUT *Prof. George K V*	EST 110 ENGINEERING GRAPHICS *Dr.Sreekanth M P*	EST 130 BASICS OF ELECTRONICS ENGINEERING *Mr.Binil Kumar K*	**EST 102 PROGRAMMING IN C PRACTICAL** *Ms. DIVYA B. / Ms. TINTU DEVASIA*	
Fr	PHT100 ENGINEERING PHYSICS TUT *Soumya John*	HUT 102 PROFESSIONAL COMMUNICATION *Ms.Ann Mathew*	EST 130 BASICS OF ELECTRONICS ENGINEERING *Mr.Binil Kumar K*	MAT 102 VECTOR CALCULUS, DIFFERENTIAL EQUATIONS AND TRANSFORMS *Prof. George K V*	**ESL 130 ELECTRICAL WORKSHOP/ ESL 130 ELECTRONICS WORKSHOP** *Ms. Tinu Francis / Mr.Binil Kumar K / F / L*	
Sa	**Special Timetable**					

AY 2021-22 S2 CSE C

Vimal Jyothi Engineering College, Chemperi, Kannur Dist., Kerala

	1	2	3	4	5	6
Mo	EST 110 ENGINEERING GRAPHICS *Mr. Rameshan K P*	HUT 102 PROFESSIONAL COMMUNICATION *Ms.Anusha Chacko*	PHT100 ENGINEERING PHYSICS *Jomy Jose*	MAT 102 VECTOR CALCULUS, DIFFERENTIAL EQUATIONS AND TRANSFORMS *Prof. George K V*	**EST 110 ENGINEERING GRAPHICS PRACTICAL** *Mr. Rameshan K P / Mr.C Francis George*	
Tu	MAT 102 VECTOR CALCULUS, DIFFERENTIAL EQUATIONS AND TRANSFORMS *Ammu Jose*	EST 102 PROGRAMMING IN C *Ms. NAYANA SURESH*	**HUT 102 PROFESSIONAL COMMUNICATION PRACTICAL** *Ms.Anusha Chacko*		PHT100 ENGINEERING PHYSICS *Jomy Jose*	EST 102 PROGRAMMING IN C *Ms. NAYANA SURESH*
We	PHT100 ENGINEERING PHYSICS *Jomy Jose*	EST 130 BASICS OF ELECTRONICS ENGINEERING *Ms.Grace John M*	EST 102 PROGRAMMING IN C TUT *Ms. NAYANA SURESH*	MAT 102 VECTOR CALCULUS DIFFERENTIAL EQUATIONS AND TRANSFORMS TUT *Ammu Jose*	**EST 102 PROGRAMMING IN C PRACTICAL** *Ms. NAMITHA P / Ms. NAYANA SURESH*	
Th	EST 130 BASICS OF ELECTRICAL ENGINEERING *Ms Athira M Thomas*	HUT 102 PROFESSIONAL COMMUNICATION *Ms.Anusha Chacko*	EST 130 BASICS OF ELECTRONICS ENGINEERING *Ms.Grace John M*	MAT 102 VECTOR CALCULUS, DIFFERENTIAL EQUATIONS AND TRANSFORMS *Ammu Jose*	EST 130 BASICS OF ELECTRICAL ENGINEERING *Ms Athira M Thomas*	MAT 102 VECTOR CALCULUS, DIFFERENTIAL EQUATIONS AND TRANSFORMS *Ammu Jose*
Fr	PHT100 ENGINEERING PHYSICS TUT *Jomy Jose*	EST 110 ENGINEERING GRAPHICS *Mr. Rameshan K P*	**ESL 130 ELECTRICAL WORKSHOP/ ESL 130 ELECTRONICS WORKSHOP** *Ms. Tinu Francis / Ms.Grace John M / F / L*		**PHL120 ENGINEERING PHYSICS LAB/ Library** *Jomy Jose / Mr. Stantly Kurian*	
Sa	**Special Timetable**					

AY 2021-22 S4 CSE A

Vimal Jyothi Engineering College, Chemperi, Kannur Dist., Kerala

	1	2	3	4	5	6
Mo	**CSL 202 DIGITAL LAB/CSL 204 OPERATING SYSTEMS LAB** Mr.Manoj K C / Mr.Binil Kumar K / Ms. VIDHYA S. S. / CS B / E			HUT 200 PROFESSIONAL ETHICS ***Vasudevan M***	CST 204 DATABASE MANAGEMENT SYSTEM ***Ms. TINTU DEVASIA***	MAT 206 GRAPH THEORY ***Ms. AMBILI M.A***
Tu	**CSL 202 DIGITAL LAB/CSL 204 OPERATING SYSTEMS LAB** Mr.Manoj K C / Mr.Binil Kumar K / Ms. VIDHYA S. S. / CS B / E			MAT 206 GRAPH THEORY TUT ***Ms. AMBILI M.A***	CST 206 OPERATING SYSTEMS ***Ms. VIDHYA S. S.***	MNC 202 CONSTITUTION OF INDIA ***Vasudevan M***
We	CST 202 COMPUTER ORGANIZATION AND ARCHITECTURE ***Ms. DIVYA K.***	CST 206 OPERATING SYSTEMS TUT ***Ms. VIDHYA S. S.***	CST 202 COMPUTER ORGANIZATION AND ARCHITECTURE ***Ms. DIVYA K.***	CST 206 OPERATING SYSTEMS ***Ms. VIDHYA S. S.***	CST 204 DATABASE MANAGEMENT SYSTEM ***Ms. TINTU DEVASIA***	HUT 200 PROFESSIONAL ETHICS ***Vasudevan M***
Th	MNC 202 CONSTITUTION OF INDIA ***Vasudevan M***	CST 202 COMPUTER ORGANIZATION AND ARCHITECTURE ***Ms. DIVYA K.***	MAT 206 GRAPH THEORY ***Ms. AMBILI M.A***	CST 204 DATABASE MANAGEMENT SYSTEM ***Ms. TINTU DEVASIA***	CST 206 OPERATING SYSTEMS ***Ms. VIDHYA S. S.***	MAT 206 GRAPH THEORY ***Ms. AMBILI M.A***
Fr	CST 202 COMPUTER ORGANIZATION AND ARCHITECTURE TUT ***Ms. DIVYA K.***	CST 204 DATABASE MANAGEMENT SYSTEM ***Ms. TINTU DEVASIA***	MAT 206 GRAPH THEORY ***Ms. AMBILI M.A***	CST 204 DATABASE MANAGEMENT SYSTEM TUT ***Ms. TINTU DEVASIA***	CST 202 COMPUTER ORGANIZATION AND ARCHITECTURE ***Ms. DIVYA K.***	CST 206 OPERATING SYSTEMS ***Ms. VIDHYA S. S.***
Sa	**Special Timetable**					

AY 2021-22 S4 CSE B

Vimal Jyothi Engineering College, Chemperi, Kannur Dist., Kerala

	1	2	3	4	5	6
Mo	CST 204 DATABASE MANAGEMENT SYSTEM TUT *Ms. SREEDHANYA M V*	CST 206 OPERATING SYSTEMS *Dr. MANOJ V. THOMAS*	CST 204 DATABASE MANAGEMENT SYSTEM *Ms. SREEDHANYA M V*	MAT 206 GRAPH THEORY *CS B*	CST 202 COMPUTER ORGANIZATION AND ARCHITECTURE *Ms. NAMITHA P*	CST 204 DATABASE MANAGEMENT SYSTEM *Ms. SREEDHANYA M V*
Tu	CST 206 OPERATING SYSTEMS *Dr. MANOJ V. THOMAS*	MNC 202 CONSTITUTION OF INDIA *Vasudevan M*	HUT 200 PROFESSIONAL ETHICS *Vasudevan M*	CST 202 COMPUTER ORGANIZATION AND ARCHITECTURE *Ms. NAMITHA P*	MAT 206 GRAPH THEORY *CS B*	CST 202 COMPUTER ORGANIZATION AND ARCHITECTURE *Ms. NAMITHA P*
We	**CSL 202 DIGITAL LAB/CSL 204 OPERATING SYSTEMS LAB** Ms.Anusha Chacko / Ms.Jerrin Yomas / Dr. MANOJ V. THOMAS / Ms. ANIT THOMAS M / I			HUT 200 PROFESSIONAL ETHICS *Vasudevan M*	MAT 206 GRAPH THEORY *CS B*	CST 206 OPERATING SYSTEMS *Dr. MANOJ V. THOMAS*
Th	CST 204 DATABASE MANAGEMENT SYSTEM *Ms. SREEDHANYA M V*	CST 202 COMPUTER ORGANIZATION AND ARCHITECTURE *Ms. NAMITHA P*	MNC 202 CONSTITUTION OF INDIA *Vasudevan M*	MAT 206 GRAPH THEORY TUT *CS B*	CST 206 OPERATING SYSTEMS *Dr. MANOJ V. THOMAS*	MAT 206 GRAPH THEORY *CS B*
Fr	CST 204 DATABASE MANAGEMENT SYSTEM *Ms. SREEDHANYA M V*	CST 206 OPERATING SYSTEMS TUT *Dr. MANOJ V. THOMAS*	CST 202 COMPUTER ORGANIZATION AND ARCHITECTURE TUT *Ms. NAMITHA P*	**CSL 202 DIGITAL LAB/CSL 204 OPERATING SYSTEMS LAB** Ms.Anusha Chacko / Ms.Jerrin Yomas / Dr. MANOJ V. THOMAS / Ms. ANIT THOMAS M / I		
Sa	**Special Timetable**					

AY 2021-22 S4 CSE C

Vimal Jyothi Engineering College, Chemperi, Kannur Dist., Kerala

	1	2	3	4	5	6
Mo	MAT 206 GRAPH THEORY *CS A*	CST 206 OPERATING SYSTEMS *Ms. NAYANA SURESH*	HUT 200 PROFESSIONAL ETHICS *Vasudevan M*	**CSL 202 DIGITAL LAB/CSL 204 OPERATING SYSTEMS LAB** Ms.Lekshmy S / Ms.Ann Mathew / Ms. SWATHI CHANDRA M T / Ms. NAYANA SURESH / I		
Tu	CST 202 COMPUTER ORGANIZATION AND ARCHITECTURE TUT *Ms. SREEDHANYA M V*	CST 204 DATABASE MANAGEMENT SYSTEM *Ms. ANIT THOMAS M*	CST 202 COMPUTER ORGANIZATION AND ARCHITECTURE *Ms. SREEDHANYA M V*	CST 206 OPERATING SYSTEMS *Ms. NAYANA SURESH*	MNC 202 CONSTITUTION OF INDIA *Vasudevan M*	CST 204 DATABASE MANAGEMENT SYSTEM *Ms. ANIT THOMAS M*
We	MAT 206 GRAPH THEORY *CS A*	CST 202 COMPUTER ORGANIZATION AND ARCHITECTURE *Ms. SREEDHANYA M V*	MNC 202 CONSTITUTION OF INDIA *Vasudevan M*	CST 204 DATABASE MANAGEMENT SYSTEM *Ms. ANIT THOMAS M*	CST 202 COMPUTER ORGANIZATION AND ARCHITECTURE *Ms. SREEDHANYA M V*	MAT 206 GRAPH THEORY *CS A*
Th	**CSL 202 DIGITAL LAB/CSL 204 OPERATING SYSTEMS LAB** Ms.Lekshmy S / Ms.Ann Mathew / Ms. SWATHI CHANDRA M T / Ms. NAYANA SURESH / I			CST 204 DATABASE MANAGEMENT SYSTEM *Ms. ANIT THOMAS M*	CST 206 OPERATING SYSTEMS *Ms. NAYANA SURESH*	CST 204 DATABASE MANAGEMENT SYSTEM TUT *Ms. ANIT THOMAS M*
Fr	MAT 206 GRAPH THEORY *CS A*	CST 206 OPERATING SYSTEMS *Ms. NAYANA SURESH*	CST 202 COMPUTER ORGANIZATION AND ARCHITECTURE *Ms. SREEDHANYA M V*	HUT 200 PROFESSIONAL ETHICS *Vasudevan M*	CST 206 OPERATING SYSTEMS TUT *Ms. NAYANA SURESH*	MAT 206 GRAPH THEORY TUT *CS A*
Sa	**Special Timetable**					

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING
SECOND ADVISORY MEETING MINUTES
S2 CSE C (2021-25 BATCH) 2021-22 ACADEMIC YEAR

ADVISORY MEETING MINUTES

Second advisory meeting of S2 CSE C was held on 17/06/2022 at 01:15 PM in S2 CSE C classroom. Meeting was chaired by Senior faculty advisor (Ms. Nayana Suresh). Faculty advisor (Ms. Namitha P), course faculties, parent representatives and students representatives attended the meeting.

Following are the minutes of the meeting.

ACADEMIC

Students Feedback:

COURSE CODE	COURSE NAME	FACULTY HANDLING	STUDENTS SUGGESTIONS
MAT102	VECTOR CALCULUS, DIFFERENTIAL EQUATIONS AND TRANSFORMS	Ms. Ammu Jose, Prof. George K V	Students & parents are satisfied with the way classes are held.
PHT100	ENGINEERING PHYSICS	Mr. Jomy Jose	Students & parents are satisfied with the way classes are held.
EST110	ENGINEERING GRAPHICS	Mr. Rameshan K P	Satisfied with the way classes are held.
EST130	BASICS OF ELECTRONICS ENGINEERING	Ms. Grace John M	Students & parents are satisfied with the way classes are held.
EST130	BASICS OF ELECTRICAL ENGINEERING	Ms. Athira M Thomas	Students & parents are satisfied with the way classes are held.
HUT102	PROFESSIONAL COMMUNICATION	Ms. Anusha Chacko	Students & parents are satisfied with the way classes are held.
EST102	PROGRAMMING IN C (THEORY & LAB)	Ms. Nayana Suresh, Ms. Namitha P	Students & parents are satisfied with the way classes are held.
PHL120	ENGINEERING PHYSICS LAB	Mr. Jomy Jose	Students & parents are satisfied with the way classes are held.
ESL130	ELECTRONICS WORKSHOP	Ms. Grace John M	Students & parents are satisfied with the way classes are held.
ESL130	ELECTRICAL WORKSHOP	Ms. Tinu Francis	Students & parents are satisfied with the way classes are held.

Students Feedback/concerns:

1. Students are satisfied with all subjects..
2. Students requested for Smart TV in the classroom instead of a projector.

Parents Feedback/concerns:

1. Both parents are satisfied with the way classes are held.

Faculty Feedback:

1. Faculty advised students to attend the booster classes if required.

NON-ACADEMIC

1. Students requested to increase the timing of mobile phone usage in Santhome Hostel.

MEMBERS ATTENDED

a. Ms. Nayana Suresh (Senior Faculty Advisor)
b. Ms. Namitha P (Faculty Advisor)
c. Parents Representative
    1. Mr. Joseph P J (P/O Alat Joseph)
    2. Mrs. Lija Saji (P/O Sayooj K S)

d. Student Representatives

1. Ameesha P Joseph
2. Abhinav P P
3. Sangeetha Ramakrishnan
4. Rithul K Rajesh
5. Joshua Sajeev
6. Mushraf Musthafa
7. Neha
8. Richa Roy
9. Sayanth P
10. Geo M Benny
11. Anto Joseph

e. Course faculty attended

1. Ms. Ammu Jose
2. Ms. Athira M Thomas
3. Ms. Nayana Suresh
4. Ms. Grace John
5. Ms. Anusha Chacko

COURSE RISK ASSESSMENT REPORT

Date and venue of the meeting	11-11-2021, Board Room
Name of the course with code	CST 307 MICROPROCESSORS AND MICROCONTROLLERS
Department focused Course Objectives	**92% PASS PERCENTAGE** **75% ATTENDANCE**
University Prescribed Subject Objectives	● Illustrate the architecture, modes of operation and addressing modes of microprocessors ● Develop 8086 assembly language programs. ● Demonstrate interrupts, its handling and programming in 8086. ● Illustrate how different peripherals (8255,8254,8257) and memory are interfaced with microprocessors. ● Outline features of microcontrollers and develop low level programs.
Risks as perceived by the course team	● Hardware related subjects may be difficult for students. ● Availability of less number of working days in the semester. ● As the subject contains assembly programming , it is difficult to make clear an idea about the subject within a short time.
Opportunities/advantages existing	● Same Subject has been handled earlier. ● Practical sessions can be provided since Lab is part of the curriculum.
Action plan points to offset the risks	AdditionalWorkshops can provide.

Name and signature of the faculty handling the course	Ancy K Sunny 
Name and signature of the course leader if any	
Name and signature of the HoD	

Vimal Jyothi Engineering College

Computer Science and Engineering (CS)

Course Name : MICROPROCESSORS AND MICROCONTROLLERS **(** CST307 **)**
Class : Semester 5 B

Ms Ancy K Sunny
Assistant Professor,
2021-22

1 . Faculty Details

Name	:	Ms Ancy K Sunny
Qualification	:	M.Tech
Department	:	CS
Permanent Address	:	Kuttikkattuthottathil (H), Machiplavu P. O, Idukki Dist. - IDUKKI TOWNSHIP - KERALA - 685561
Phone Number	:	9496338263
Email ID	:	ancyksunny@vjec.ac.in
Specimen Signature	:	_____

HOD

2 . Course Allotted

Allotted Duty	Course Title	Course Code
Theory 1	MICROPROCESSORS AND MICROCONTROLLERS	CST307

3 . Academic calendar 2021-22 (Semester 5)

Date	Day	Event
20 Oct 2021	WEDNESDAY	Term Start Date
23 Oct 2021	SATURDAY	Fourth Saturday
1 Nov 2021	MONDAY	7th Semester First Internal Examination
2 Nov 2021	TUESDAY	7th Semester First Internal Examination
3 Nov 2021	WEDNESDAY	7th Semester First Internal Examination
4 Nov 2021	THURSDAY	Deepavali
13 Nov 2021	SATURDAY	Second Saturday
27 Nov 2021	SATURDAY	Fourth Saturday
6 Dec 2021	MONDAY	7th Semester 2nd Internal Exam
7 Dec 2021	TUESDAY	7th Semester 2nd Internal Exam
11 Dec 2021	SATURDAY	Second Saturday
20 Dec 2021	MONDAY	First Internal Examination - S3 and S5
21 Dec 2021	TUESDAY	First Internal Examination - S3 and S5
22 Dec 2021	WEDNESDAY	First Internal Examination - S3 and S5
22 Dec 2021	WEDNESDAY	Christmas Holidays
23 Dec 2021	THURSDAY	Christmas Holidays
24 Dec 2021	FRIDAY	Christmas Holidays
25 Dec 2021	SATURDAY	*Christmas*
25 Dec 2021	SATURDAY	Christmas Holidays
26 Dec 2021	SUNDAY	Christmas Holidays
27 Dec 2021	MONDAY	Christmas Holidays
28 Dec 2021	TUESDAY	Christmas Holidays
29 Dec 2021	WEDNESDAY	Christmas Holidays

Date	Day	Event
30 Dec 2021	THURSDAY	Christmas Holidays
31 Dec 2021	FRIDAY	Christmas Holidays
1 Jan 2022	SATURDAY	Christmas Holidays
1 Jan 2022	SATURDAY	Christmas Holidays
2 Jan 2022	SUNDAY	Christmas Holidays
7 Jan 2022	FRIDAY	S1 First Internal Exam
8 Jan 2022	SATURDAY	S1 First Internal Exam
9 Jan 2022	SUNDAY	S1 First Internal Exam
10 Jan 2022	MONDAY	S1 First Internal Exam
13 Jan 2022	THURSDAY	S1 First Internal Exam
26 Jan 2022	WEDNESDAY	Republic Day
5 Feb 2022	SATURDAY	2nd Internal Examination
6 Feb 2022	SUNDAY	2nd Internal Examination
7 Feb 2022	MONDAY	2nd Internal Examination
8 Feb 2022	TUESDAY	2nd Internal Examination
10 Feb 2022	THURSDAY	2nd Internal Examination
11 Feb 2022	FRIDAY	2nd Internal Examination
12 Feb 2022	SATURDAY	2nd Internal Examination
13 Feb 2022	SUNDAY	2nd Internal Examination
14 Feb 2022	MONDAY	2nd Internal Examination
1 Mar 2022	TUESDAY	Shivratri
31 Mar 2022	THURSDAY	Term End Date

5 . Department Details

5 . 1 Preliminary Information

PROGRAM EDUCATIONAL OBJECTIVES

Peo 1 : Graduates will achieve broad and in-depth knowledge of Computer Science and Engineering relating to industrial practices and research to analyze the practical problems and think creatively to generate innovative solutions using appropriate technologies.

Peo 2 : Graduates will make valid judgement, synthesize information from a range of sources and communicate them in sound ways appropriate to their discipline.

Peo 3 : Graduates will sustain intellectual curiosity and pursue lifelong learning not only in areas that are relevant to Computer Science, but also that are important to society.

Peo 4 : Graduates will adapt to different roles and demonstrate leaderships in global writing environment by respecting diversity, professionalism and ethical practices

PROGRAM OUTCOMES(PO's)

1. **Engineering knowledge :** Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems

2. **Problem Analysis :** Identify, formulate, review research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences

3. **Design/ Development of Solutions :** Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations

4. **Conduct investigations of complex problems :** Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions

5. **Modern tool usage :** Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations

6. **The engineer and society :** Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice

7. **Environment and sustainability :** Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development

8. **Ethics :** Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice

9. **Individual and team work :** Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings

10. **Communication :** Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions

11. **Project management and finance :** Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments

12. **Life-long learning :** Recognize the need for, and have the preparation and ability to engage in Independent and life-long learning in the broadest context of technological change

PROGRAM SPECIFIC OUTCOMES(PSO's)

PSO 1 : An ability to apply development principles to analyze and design complex software and systems containing hardware and software components of varying complexity.

PSO 2 : An ability to apply mathematical foundations, algorithmic principles, and computer science theory in the modeling and design of computer-based systems in a way that demonstrates comprehension of the trade-offs involved in design choices.

6 . Course Information

6 . 1 Course Content

Title of the Course : MICROPROCESSORS AND MICROCONTROLLERS
Semester : 5 **Academic Year : 2021-22**

Subject Code : CST307	IA Marks : 50
Hours/week : 3	Total Hours : 45
Exam Hours : 3	Exam Marks : 100
Course Plan Author : Ancy K Sunny	Planned Date : 2021-10-27
Approved by : Dr Jeethu V Devasia	Approved Date : 2021-10-27

Objectives:

1 .

Course Outcomes (COs) :

1 . Illustrate the architecture, modes of operation and addressing modes of\nmicroprocessors (Cognitive knowledge: Understand)

2 . Develop 8086 assembly language programs. (Cognitive Knowledge Level: Apply)

3 . Demonstrate interrupts, its handling and programming in 8086. (Cognitive Knowledge\nLevel: Apply))

4 . Illustrate how different peripherals (8255,8254,8257) and memory are interfaced with \nmicroprocessors. (Cognitive Knowledge Level: Understand)

5 . Outline features of microcontrollers and develop low level programs. (Cognitive\nKnowledge Level: Understand)

6 . Course Information

6 . 1 . 1 Course Syllabus

Objectives:
Title of the Course : MICROPROCESSORS AND MICROCONTROLLERS
Subject Code : CST307

Module 1

Evolution of microprocessors :

8085 microprocessor (-Basic Architecture only), 8086 microprocessor – Architecture and signals, Physical Memory organization, Minimum and maximum mode of 8086 system and timings, Comparison of 8086 and 8088, Machine language Instruction format

Module 2

Addressing modes and instructions :

Addressing Modes of 8086, Instruction set – data copy /transfer instructions, arithmetic instructions, logical instructions, string manipulation instructions, branch instructions, unconditional and conditional branch instruction, flag manipulation and processor control instructions, Assembler Directives and operators, Assembly Language Programming with 8086

Module 3

Stack and interrupts :

Stack structure of 8086, programming using stack, Interrupts , Types of Interrupts and Interrupt Service Routine, Handling Interrupts in 8086, Interrupt programming, , Programmable Interrupt Controller , 8259, Architecture (Just mention the control word, no need to memorize the control word), Interfacing Memory with 8086

Module 4

Interfacing chips :

Programmable Peripheral Input/output port 8255 , Architecture and modes of operation, Programmable interval timer 8254, Architecture and modes of operation, DMA controller 8257 Architecture (Just mention the control word, no need to memorize the control word of 8254 and 8257)

Module 5

Microcontrollers :

8051 Architecture, Register Organization, Memory and I/O addressing, Interrupts and Stack, 8051 Addressing Modes, Instruction Set, data transfer instructions, arithmetic instructions, logical instructions, Boolean instructions, control transfer instructions, Simple programs

6 . Course Information

6 . 1 . 2 Text Books and Reference Books

TEXT BOOKS :

1 . Bhurchandi and Ray, Advanced Microprocessors and Peripherals, Third Edition McGraw Hill

2 . Raj Kamal, Microcontrollers: Architecture, Programming, Interfacing and System Design, Pearson Education

3 . Ramesh Gaonkar, Microprocessor Architecture, Programming, and Applications with the 8085, Penram International Publishing Pvt. Ltd

REFERENCE BOOKS :

1 . Barry B. Brey, The Intel Microprocessors – Architecture, Programming and Interfacing, Eighth Edition, Pearson Education

2 . A. NagoorKani, Microprocessors and Microcontrollers, Second Edition, Tata McGraw Hill

3 . Douglas V. Hall, SSSP Rao, Microprocessors and Interfacing, Third Edition, McGrawHill Education

6 . Course Information

6 . 2

Semester : 5 **Section :** B **Course :** MICROPROCESSORS AND MICROCONTROLLERS

Period	Planned			Execution		
	Date	Topic	Source material to be referred	Date	Topic	Source material to be referred
			1			
1	2021-11-15	8085 microprocessor (- Basic Architecture only)	-	2021-11-18	8085 microprocessor (- Basic Architecture only)	Text 1
2	2021-11-16	8085 microprocessor (- Basic Architecture only)	-	2021-11-18	8085 microprocessor (- Basic Architecture only)	Text 1
3	2021-11-17	8086 microprocessor – Architecture and signals	-	2021-11-19	8086 microprocessor – Architecture andsignals	Text 1
4	2021-11-18	8086 microprocessor – Architecture and signals	-	2021-11-19	8086 microprocessor – Architecture andsignals	Text 1
5	2021-11-22	Physical Memory organization	-	2021-11-20	Physical Memory organization	Text 1
6	2021-11-23	Physical Memory organization	-	2021-11-22	Physical Memory organization	Text 1
7	2021-11-24	Minimum and maximum mode of 8086 system and timings	-	2021-11-15	Minimum and maximum mode of 8086 systemand timings	Text 1
8	2021-11-25	Comparison of 8086 and 8088	-	2021-11-22	Comparison of 8086 and 8088	Text 1
9	2021-11-29	Machine language Instruction format	-	2021-11-22	Machine language Instruction format	Text 1
			2			
10	2021-12-01	Addressing Modes of 8086, Instruction set – data copy / transfer instructions	-	2021-11-24	Addressing Modes of 8086, Instruction set – data copy / transfer instructions	Text 1
11	2021-12-02	arithmetic instructions	-	2021-11-25	arithmeticinstructions	Text 1
12	2021-12-06	logical instructions	-	2021-11-26	logical instructions	Text 1
13	2021-12-07	string manipulation instructions	-	2021-12-03	string manipulation instructions	-
14	2021-12-08	branch instructions	-	2021-11-29	branch instructions	Text 1
15	2021-12-09	unconditional and conditional branch instruction	-	2021-11-29	unconditional and conditional branch instruction	Text 1
16	2021-12-16	flag manipulation and processor control instructions	-	2021-11-29	flag manipulation and processor controlinstructions	Text 1

17	2021-12-20	Assembler Directives and operators	-	2021-12-06	Assembler Directives and operators	Text 1
18	2021-12-21	Assembly Language Programming with 8086	-	2021-11-29	Assembly Language Programming with8086	Text 1
3						
19	2022-01-03	Stack structure of 8086, programming using stack	-	2021-12-06	Assembly Language Programming with8086	Text 1
20	2022-01-04	Interrupts , Types of Interrupts and Interrupt Service Routine	-	2021-12-06	Stack structure of 8086, programming using stack	Text 1
21	2022-01-05	Handling Interrupts in 8086, Interrupt programming	-	2021-12-08	Interrupts, Types of Interrupts andInterrupt Service Routine	Text 1
22	2022-01-06	Stack structure of 8086	Text 1	2021-12-13	Handling Interrupts in 8086, Interrupt programming	Text 1
23	2022-01-10	Programmable Interrupt Controller	-	2022-01-03	Programmable Interrupt Controller	-
24	2022-01-11	8259	-	2022-01-03	8259, Architecture (Just mention the control word, noneed to memorize the control word)	Text 1
25	2022-01-12	Architecture (Just mention the control word	-	2022-01-03	Architecture (Just mention the control word, noneed to memorize the control word)	Text 1
26	2022-01-13	no need to memorize the control word)	-	2022-01-05	Interfacing Memory with 8086	Text 1
27	2022-01-17	Interfacing Memory with 8086	-	2022-01-07	Interfacing Memory with 8086	Text 1
4						
28	2022-01-19	Programmable Peripheral Input/output port 8255	-	2022-01-10	Programmable Peripheral Input/output port 8255	Text 1
29	2022-01-20	Programmable Peripheral Input/output port 8255	-	2022-01-11	Programmable Peripheral Input/output port 8255	-
30	2022-01-24	Architecture and modes of operation	-	2022-01-13	Architecture and modes of operation	Text 1
31	2022-01-25	Programmable interval timer 8254	-	2022-01-15	Programmable interval timer 8254	Text 1
32	2022-01-26	Architecture and modes of operation	-	2022-01-15	Architecture and modes of operation	Text 1
33	2022-01-27	DMA controller 8257 Architecture (Just mention the control word	-	2022-01-17	DMA controller8257 Architecture (Just mention the control word, no need to memorize the control wordof 8254 and 8257)	Text 1
34	2022-01-31	no need to memorize the control word of 8254 and 8257)	-	2022-01-17	DMA controller8257 Architecture (Just mention the control word, no need to memorize the control wordof 8254 and 8257)	Text 1

			5			
35	2022-02-07	8051 Architecture, Register Organization	-	2022-01-20	8051 Architecture, Register Organization	Text 1
36	2022-02-08	Memory and I/O addressing	-	2022-01-24	Memory and I/O addressing	Text 1
37	2022-02-09	Interrupts and Stack	-	2022-01-24	Interrupts andStack	Text 1
38	2022-02-10	8051 Addressing Modes	-	2022-01-27	8051 Addressing Modes	Text 1
39	2022-02-14	Instruction Set	-	2022-01-29	Instruction Set	Text 1
40	2022-02-15	data transfer instructions	-	2022-01-29	data transfer instructions	Text 1
41	2022-02-16	arithmetic instructions	-	2022-01-31	arithmeticinstructions	Text 1
42	2022-02-17	logical instructions	-	2022-01-31	logical instructions	Text 1
43	2022-02-21	Boolean instructions	-	2022-01-31	Boolean instructions	Text 1
44	2022-02-22	control transfer instructions	-	2022-01-31	control transfer instructions	Text 1
45	2022-02-23	Simple programs	-	2022-02-02	Simple programs	Text 1

6 . Course Information

6 . 2 . 1 Compliance Report

Semester : 5 **Section :** B **Course :** MICROPROCESSORS AND MICROCONTROLLERS

Module No.	# of Classes Planned(till date)	Planned Effort(till date)	# of Classes Executed(till date)	Actual Efforts(till date)	% Coverage
1	9	9hrs 0min	9	9hrs 0min	100.0
2	10	10hrs 0min	10	10hrs 0min	100.0
3	8	8hrs 0min	8	8hrs 0min	100.0
4	7	7hrs 0min	7	7hrs 0min	100.0
5	11	11hrs 0min	11	11hrs 0min	100.0

HOD

6 . Course Information

6 . 2 . 2 CO PO Mapping

Slight (Low) = 1 , Moderate (Medium) = 2 , Substantial (High) = 3 .

CO/ PO	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
CO 1	2	2	2									2
CO 2	3	3	3	3								3
CO 3	3	3	3	3								3
CO 4	2	2	2									2
CO 5	3	3	3	3								3

6 . Course

6 . 2 . 3 CO-PSO Mapping

Slight (Low) = 1 , Moderate (Medium) = 2 , Substantial (High) = 3 .

CO/PSO	PSO 1	PSO 2
CO 1	3	
CO 2	3	
CO 3	3	
CO 4	3	
CO 5	3	

6 . Course Information

6 . 3 Other Assessment

ASSIGNMENT : 1

Semester:5-Scheme 2019

Subject : MICROPROCESSORS AND MICROCONTROLLERS (CST307)

Faculty : Ancy K Sunny

Max Marks: 15

Answer Any 1 Questions					
Q.No			**Max Marks**	**CO**	**BT/ CL**
1		*What are conditional and control flags in 8086?*	5	1	L2
1		*Write an assembly language program to demonstrate an automatic traffic light at a 4-way junction.*	10	4	L3
2		*What is the difference between minimum and maximum modes of 8086?*	5	1	L2
2		*Interface 8255 and write a program to generate a Square waveform at one of its output pin.*	10	4	L3
3		*Explain the concept of pipelining in 8086. Discuss its advantages and disadvantages with an example.*	5	1	L2

3		*Write an Assembly language program to find the largest and smallest of given numbers.*	10	2	L3
4		*What is the difference between CALL and JMP instruction of 8086 microprocessor?*	5	2	L2
4		*Draw and explain the schematic for Interfacing the Key board display controller 8279 with microprocessor.*	10	4	L3
5		*Write an Assembly language program to find average of "N" given numbers.*	5	2	L2
5		*Write an Assembly language program of 8086 to find factorial of a number using subroutine.*	10	2	L3
6		*Explain the purpose of the I/O instructions IN and OUT.*	5	2	L2
6		*Write an Assembly language program for Generation of Sawtooth waveform.*	10	4	L3
7		*How 16-Bit address is converted into 20-Bit address in 8086?*	5	1	L2

7		*Write an Assembly language program for Generation of Triangular waveform.*	10	4	L3
8		*If the stack segment register contains 3000H and the stack pointer register contains 8434H, what is the physical address of the top of the stack?*	5	1	L2
8		*Write an Assembly language program to find smallest Number in a block of data. Length of block is 0A. Store the maximum in location result.*	10	2	L3
9		*Write an assembly language program to find average of "N" given numbers.*	5	2	L3
9		*Write an assembly language program to convert the given lower case character into upper case in 8086.*	10	2	L3
10		*List any four stack related instructions of 8086 microprocessor and explain any two instruction.*	5	2	L2
10		*Write an assembly language program to interface ADC.*	10	4	L3
11		*Explain the purpose of the I/O instructions IN and OUT.*	5	2	L2

11		*Write an assembly language program to Sort of one dimension array in ascending and descending order.*	10	2	L3
12		*Compare Procedure & Macro.*	5	2	L2
12		*Write an assembly language program to interface DAC.*	10	4	L3

Evaluation

USN	Name	Present (P) / Absent (Ab)	Q11		Q12		Q1		Q2		Q3		Q4		Q5		Q6		Q7		Q8		Q9		Q10		IA Total	BT/CL
			a	b	a	b	a	b	a	b	a	b	a	b	a	b	a	b	a	b	a	b	a	b	a	b		
LVML19CS116	Anurag A M	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
LVML19CS117	Aromal Prakash K V	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	2	4	6	No Level
LVML19CS118	Kiran P P	P	4	7	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	11	Apply
VML19CS002	Abhijai K	P	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	5	No Level
VML19CS005	Achal Dev P	P	2	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	4	No Level
VML19CS007	Adila Farha P K	P	3	6	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	9	Apply
VML19CS008	Adithya T K	P	0	0	0	0	0	0	4	8	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	12	Apply
VML19CS010	Adwaid Sahadevan M	P	2	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	4	No Level
VML19CS012	Adwetha Falgunan	P	0	0	4	9	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	13	Apply
VML19CS015	Akhil Kumar K	P	0	0	2	4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	6	No Level
VML19CS017	Akshay Jayachandran V V	P	0	0	3	6	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	9	Apply
VML19CS019	Alan Saji	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS021	Aleena Mathews	P	0	0	4	9	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	13	Apply
VML19CS023	Alisha Mathew	P	0	0	0	0	2	4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	6	No Level
VML19CS024	Amalraj P	P	0	0	0	0	3	4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7	Understand
VML19CS026	Anagha P P	P	0	0	0	0	3	7	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	10	Apply
VML19CS028	Aneesha S	P	0	0	0	0	2	8	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	10	Apply
VML19CS030	Anjima Govindan	P	0	0	0	0	3	4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7	Understand

USN	Name	Present (P) / Absent (Ab)	Q11		Q12		Q1		Q2		Q3		Q4		Q5		Q6		Q7		Q8		Q9		Q10		IA Total	BT/CL
			a	b	a	b	a	b	a	b	a	b	a	b	a	b	a	b	a	b	a	b	a	b	a	b		
VML19CS031	Annapoorna K K	P	0	0	0	0	0	0	3	4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7	Understand
VML19CS035	Anupama K V	P	0	0	0	0	0	0	3	4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7	Understand
VML19CS037	Anusree Venu	P	0	0	0	0	0	0	3	9	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	12	Apply
VML19CS039	Arya Sajiv	P	0	0	0	0	0	0	3	6	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	9	Apply
VML19CS040	Ashwin S Nambiar	P	0	0	0	0	0	0	3	6	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	9	Apply
VML19CS042	Athira Das	P	0	0	0	0	0	0	0	0	3	4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7	Understand
VML19CS044	Aysha Nahadha	P	2	4	0	0	0	0	2	4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	6	No Level
VML19CS046	Darsan Dinesh	P	0	0	0	0	0	0	0	0	2	5	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7	Apply
VML19CS048	Deekshith K K	P	0	0	0	0	0	0	0	0	2	4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	6	No Level
VML19CS050	Devika C	P	0	0	0	0	0	0	0	0	3	8	0	0	0	0	0	0	0	0	0	0	0	0	0	0	11	Apply
VML19CS052	Diya P	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS054	Don Mariya	P	0	0	0	0	0	0	0	0	0	0	4	6	0	0	0	0	0	0	0	0	0	0	0	0	10	Apply
VML19CS055	Eaby Thomas C	P	0	0	0	0	0	0	0	0	0	0	3	6	0	0	0	0	0	0	0	0	0	0	0	0	9	Apply
VML19CS058	Farisa K P	P	0	0	0	0	0	0	0	0	0	0	4	8	0	0	0	0	0	0	0	0	0	0	0	0	12	Apply
VML19CS061	Harold Prakash	P	0	0	0	0	0	0	0	0	0	0	3	5	0	0	0	0	0	0	0	0	0	0	0	0	8	Apply
VML19CS063	Janvin Joseph	P	0	0	0	0	0	0	0	0	0	0	0	0	4	8	0	0	0	0	0	0	0	0	0	0	12	Apply
VML19CS065	Jithin Jose	P	0	0	0	0	0	0	0	0	0	0	0	0	3	7	0	0	0	0	0	0	0	0	0	0	10	Apply
VML19CS067	Kavya Pushpan	P	0	0	0	0	0	0	0	0	0	0	0	0	5	9	0	0	0	0	0	0	0	0	0	0	14	Apply
VML19CS069	Kiran Valsalan Nair	P	0	0	0	0	0	0	0	0	0	0	0	0	4	7	0	0	0	0	0	0	0	0	0	0	11	Apply
VML19CS071	Mary Joy	P	0	0	0	0	0	0	0	0	0	0	0	0	4	9	0	0	0	0	0	0	0	0	0	0	13	Apply
VML19CS074	Nathasha K V	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	5	5	0	0	0	0	0	0	0	0	10	Apply
VML19CS075	Nihal O	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	2	4	0	0	0	0	0	0	0	0	6	No Level
VML19CS078	Nirmal Shaju	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	3	6	0	0	0	0	0	0	0	0	9	Apply
VML19CS080	Puliyile Kandi Muhammed Jassim	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	2	2	0	0	0	0	0	0	0	0	4	No Level
VML19CS081	PV Gayathri	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	3	6	0	0	0	0	0	0	0	0	9	Apply
VML19CS083	Ranjul Arumadi	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	5	8	0	0	0	0	0	0	13	Apply
VML19CS084	Rhea Renjith	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	3	3	0	0	0	0	0	0	6	Apply
VML19CS086	Roby K S	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	2	3	0	0	0	0	0	0	5	No Level
VML19CS089	Sanand Chandran	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	2	4	0	0	0	0	0	0	6	No Level
VML19CS091	Sanjuktha Sanjay	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	4	6	0	0	0	0	0	0	10	Apply
VML19CS094	Sharanya Ullas	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	4	6	0	0	0	0	10	Apply
VML19CS096	Shijas P	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	2	4	0	0	0	0	6	No Level
VML19CS098	Shradha Sujith	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	4	9	0	0	0	0	13	Apply
VML19CS101	Sidharth A S	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	4	6	0	0	0	0	10	Apply
VML19CS103	Sidharth Suresh Nambiar	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	3	0	0	0	0	4	No Level

USN	Name	Present (P) / Absent (Ab)	Q11		Q12		Q1		Q2		Q3		Q4		Q5		Q6		Q7		Q8		Q9		Q10		IA Total	BT/CL
			a	b	a	b	a	b	a	b	a	b	a	b	a	b	a	b	a	b	a	b	a	b	a	b		
VML19CS104	Sneha Anil	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	5	9	0	0	14	Apply
VML19CS106	Sona Jose	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	5	8	0	0	13	Apply
VML19CS108	Sooraj Mohan	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	5	9	0	0	14	Apply
VML19CS110	Sreevedh Hareesh	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	4	8	0	0	12	Apply
VML19CS112	Theerth M	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	2	0	0	3	No Level
VML19CS114	Vismaya Vinoth Kumar	P	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	3	6	9	Apply

Q.No			Max Marks	CO	BT/ CL
1		Assignment Test.	15	4	L2

Evaluation

USN	Name	Present (P) / Absent (Ab)	Q1	IA Total	BT/CL
LVML19CS116	Anurag A M	P	15	15	Understand
LVML19CS117	Aromal Prakash K V	P	15	15	Understand
LVML19CS118	Kiran P P	P	15	15	Understand
VML19CS002	Abhijai K	P	15	15	Understand
VML19CS005	Achal Dev P	P	15	15	Understand
VML19CS007	Adila Farha P K	P	15	15	Understand
VML19CS008	Adithya T K	P	15	15	Understand
VML19CS010	Adwaid Sahadevan M	P	15	15	Understand
VML19CS012	Adwetha Falgunan	P	15	15	Understand
VML19CS015	Akhil Kumar K	P	15	15	Understand
VML19CS017	Akshay Jayachandran V V	P	15	15	Understand
VML19CS019	Alan Saji	P	15	15	Understand
VML19CS021	Aleena Mathews	P	15	15	Understand
VML19CS023	Alisha Mathew	P	15	15	Understand
VML19CS024	Amalraj P	P	15	15	Understand
VML19CS026	Anagha P P	P	15	15	Understand
VML19CS028	Aneesha S	P	15	15	Understand
VML19CS030	Anjima Govindan	P	15	15	Understand
VML19CS031	Annapoorna K K	P	15	15	Understand
VML19CS035	Anupama K V	P	15	15	Understand
VML19CS037	Anusree Venu	P	15	15	Understand
VML19CS039	Arya Sajiv	P	15	15	Understand
VML19CS040	Ashwin S Nambiar	P	15	15	Understand
VML19CS042	Athira Das	P	15	15	Understand
VML19CS044	Aysha Nahadha	P	15	15	Understand
VML19CS046	Darsan Dinesh	P	15	15	Understand

USN	Name	Present (P) / Absent (Ab)	Q1	IA Total	BT/CL
VML19CS048	Deekshith K K	P	15	15	Understand
VML19CS050	Devika C	P	15	15	Understand
VML19CS052	Diya P	P	15	15	Understand
VML19CS054	Don Mariya	P	15	15	Understand
VML19CS055	Eaby Thomas C	P	15	15	Understand
VML19CS058	Farisa K P	P	15	15	Understand
VML19CS061	Harold Prakash	P	15	15	Understand
VML19CS063	Janvin Joseph	P	15	15	Understand
VML19CS065	Jithin Jose	P	15	15	Understand
VML19CS067	Kavya Pushpan	P	15	15	Understand
VML19CS069	Kiran Valsalan Nair	P	15	15	Understand
VML19CS071	Mary Joy	P	15	15	Understand
VML19CS074	Nathasha K V	P	15	15	Understand
VML19CS075	Nihal O	P	15	15	Understand
VML19CS078	Nirmal Shaju	P	15	15	Understand
VML19CS080	Puliyile Kandi Muhammed Jassim	P	15	15	Understand
VML19CS081	PV Gayathri	P	15	15	Understand
VML19CS083	Ranjul Arumadi	P	15	15	Understand
VML19CS084	Rhea Renjith	P	15	15	Understand
VML19CS086	Roby K S	P	15	15	Understand
VML19CS089	Sanand Chandran	P	15	15	Understand
VML19CS091	Sanjuktha Sanjay	P	15	15	Understand
VML19CS094	Sharanya Ullas	P	15	15	Understand
VML19CS096	Shijas P	P	15	15	Understand
VML19CS098	Shradha Sujith	P	15	15	Understand
VML19CS101	Sidharth A S	P	15	15	Understand
VML19CS103	Sidharth Suresh Nambiar	P	15	15	Understand
VML19CS104	Sneha Anil	P	15	15	Understand
VML19CS106	Sona Jose	P	15	15	Understand
VML19CS108	Sooraj Mohan	P	15	15	Understand
VML19CS110	Sreevedh Hareesh	P	15	15	Understand
VML19CS112	Theerth M	P	15	15	Understand
VML19CS114	Vismaya Vinoth Kumar	P	15	15	Understand

2 Scheme of Evaluation

VIMAL JYOTHI ENGINEERING COLLEGE, CHEMPERI

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

S5 SEMESTER B.TECH (2019-2023 BATCH)

CST 307 MICROPROCESSOR AND MICROCONTROLLERS

ASSIGNMENT TEST

Date : 24-01-2022 Max. Marks : 60
Time : 10:00 AM Duration : 2hours

Answer All Questions

Qn No.	Question	CO	Blooms Level	Marks
1	What are the three different I/O modes supported by 8255? [PI : 3.2.1]	4	2	3
2	List any four features of 8257 DMA Controller. [PI : 3.2.1]	4	2	3
3	Describe the control word format for the BSR mode of 8255. [PI : 3.2.1]	4	2	3
4	Briefly describe the control word format of 8255 PPI. [PI : 3.2.1]	4	2	3
5	What is DMA? State the sequence of operations performed by a DMA controller in a DMA transfer operation. [PI : 3.2.1]	4	2	3
	Part B			
6	Explain the architecture and modes of operation of 8254/8253 programmable Timer/Counter with necessary diagrams. [PI : 3.2.1]	4	2	14
7	With a neat diagram describe the architecture of 8255. [PI : 3.2.1]	4	2	14
8	With a neat diagram describe the architecture of 8257. [PI : 3.2.1]	4	2	14

CO4 : Illustrate how different peripherals (8255,8254,8257) and memory are interfaced with microprocessors.

*Level: Knowledge level based on Blooms Taxonomy[L2. Understanding

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CST 307 MICROPROCESSOR AND MICROCONTROLLERS

ASSIGNMENT TEST

Date : 24-01-2022
Time : 10:00 AM

Max. Marks : 60
Duration : 2hours

Answer all the questions

PART A (3 Marks)

1. What are the three different I/O modes supported by 8255?

   BSR mode 1 marks

   I/O mode 2 marks

2. List any four features of 8257 DMA Controller.

   Any three feature : 1 marks each

3. Describe the control word format for the BSR mode of 8255.

   Figure 2 marks

   Explanation : 1 mark

4. What are the purposes of the signals DRQ, TC and MARK in 8257?

   1 marks each

5. Briefly describe the control word format of 8255 PPI.

   Figure 2 marks

   Explanation : 1 mark

6. What is DMA? State the sequence of operations performed by a DMA controller in a DMA transfer operation.

DMA : 1 mark

DMA transfer operation : 2 marks

PART B(14 Marks)

7. Explain the architecture and modes of operation of 8254/8253 programmable Timer/ Counter with necessary diagrams.

Architecture : 10 marks

Modes of operation : 4 marks

8. With a neat diagram describe the architecture of 8255.

Figure : 10 marks

Explanation : 4 marks

9. With a neat diagram describe the architecture of 8257.

Figure : 10 marks

Explanation : 4 marks

6 . Course Information

6 . 4 Internal Assessment

Internal : 1

Semester:5-Scheme 2019 Date : 14/12/2021

Subject : MICROPROCESSORS AND MICROCONTROLLERS (CST307) Time : 13:30 - 15:30

Faculty : Ancy K Sunny Max Marks: 60

Part A					
Answer all questions					
Q.No			**Max Marks**	**CO**	**BT/CL**
1		*Compare 8085 and 8086 microprocessors in terms of width of data bus and memory addressing capacity. [PI 3.3.1]*	3	1	L2
2		*8086 has CS,DS,ES and SS in its architecture. Identify its uses. [PI 3.1.6]*	3	1	L2
3		*8086 has a queue in its Bus Interface Unit. What is the size of that queue? Discuss the function of the instruction queue in 8086. [PI 1.3.1]*	3	1	L2
4		*What is an assembler? Discuss the use of assembler directive. [PI 1.3.1]*	3	2	L1
5		*Write an 8086 assembly language program to add 05 and 03. Store the result in the BH register.[PI 3.2.1]*	3	2	L3

6		*What is ISR? Differentiate ISR and normal subroutine. [PI 1.3.1]*	3	3	L2
Part B					
Answer any 6 questions					
Q.No			**Max Marks**	**CO**	**BT/CL**
7		*Explain the physical memory organization of 8086 with a neat diagram. How does the 8086 processor access a word from an odd memory location? How many memory cycles does it take? [PI 1.3.1]*	7	1	L2
8		*How is the physical address calculated in 8086? Calculate the physical address of an instruction. The contents of segment registers are given as CS:1050H , DS : 2005H, SS : 3005H and IP : 5550H. [PI 1.3.1]*	7	1	L3
9		*Draw and explain the architecture of the 8086 microprocessor. [PI 1.3.1]*	7	1	L2

10		*Identify different methods to specify the operands in 8086 microprocessor instructions. Explain each of them. [PI 2.1.2]*	7	2	L3
11		*What are the various classifications of instructions based on the operations they perform? Explain each with examples. [PI 1.3.1]*	7	2	L2
12		*Write an 8086 assembly language program for finding the sum of the squares of first N natural numbers. Value of N is stored at location 2500H and the result should be stored at location 2600H. [PI 2.1.2]*	7	2	L3
13		*Explain the procedure to handle an Interrupt in 8086. [PI 2.1.2]*	7	3	L2

Evaluation

USN	Name	Present (P) / Absent (Ab)	Q11	Q12	Q13	Q1	Q2	Q3	Q4	Q5	Q6	Q7	Q8	Q9	Q10	IA Total	BT/CL
LVML19CS116	Anurag A M	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
LVML19CS117	Aromal Prakash K V	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
LVML19CS118	Kiran P P	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS002	Abhijai K	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS005	Achal Dev P	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level

USN	Name	Present (P) / Absent (Ab)	Q11	Q12	Q13	Q1	Q2	Q3	Q4	Q5	Q6	Q7	Q8	Q9	Q10	IA Total	BT/CL
VML19CS007	Adila Farha P K	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS008	Adithya T K	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS010	Adwaid Sahadevan M	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS012	Adwetha Falgunan	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS015	Akhil Kumar K	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS017	Akshay Jayachandran V V	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS019	Alan Saji	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS021	Aleena Mathews	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS023	Alisha Mathew	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS024	Amalraj P	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS026	Anagha P P	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS028	Aneesha S	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS030	Anjima Govindan	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS031	Annapoorna K K	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS035	Anupama K V	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS037	Anusree Venu	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS039	Arya Sajiv	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS040	Ashwin S Nambiar	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS042	Athira Das	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS044	Aysha Nahadha	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS046	Darsan Dinesh	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS048	Deekshith K K	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS050	Devika C	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS052	Diya P	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS054	Don Mariya	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS055	Eaby Thomas C	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS058	Farisa K P	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS061	Harold Prakash	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS063	Janvin Joseph	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS065	Jithin Jose	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS067	Kavya Pushpan	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS069	Kiran Valsalan Nair	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS071	Mary Joy	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS074	Nathasha K V	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS075	Nihal O	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS078	Nirmal Shaju	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level

USN	Name	Present (P) / Absent (Ab)	Q11	Q12	Q13	Q1	Q2	Q3	Q4	Q5	Q6	Q7	Q8	Q9	Q10	IA Total	BT/CL
VML19CS080	Puliyile Kandi Muhammed Jassim	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS081	PV Gayathri	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS083	Ranjul Arumadi	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS084	Rhea Renjith	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS086	Roby K S	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS089	Sanand Chandran	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS091	Sanjuktha Sanjay	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS094	Sharanya Ullas	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS096	Shijas P	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS098	Shradha Sujith	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS101	Sidharth A S	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS103	Sidharth Suresh Nambiar	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS104	Sneha Anil	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS106	Sona Jose	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS108	Sooraj Mohan	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS110	Sreevedh Hareesh	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS112	Theerth M	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level
VML19CS114	Vismaya Vinoth Kumar	Ab	0	0	0	0	0	0	0	0	0	0	0	0	0	0	No Level

2 Scheme of Evaluation

VIMAL JYOTHI ENGINEERING COLLEGE, KANNUR

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

SEMESTER 5

CST 307 - Microprocessors and Microcontrollers

Internal Assessment 1

Scheme for valuation

Part A

1. Compare 8085 and 8086 microprocessors in terms of width of data bus and memory addressing capacity.
   1.5 mark for each microprocessor
2. 8086 has CS,DS,ES and SS in its architecture. Identify its uses.
   CS : 1 mark
   DS : 1 mark
   SS : 0.5 mark
   ES : 0.5 mark

3. 8086 has a queue in its Bus Interface Unit. What is the size of that queue? Discuss the function of the instruction queue in 8086.
   Size : 1 mark
   Use : 2 marks

4. What is an assembler? Discuss the use of assembler directive.
   Assembler : 2 marks
   Assembler directives : 1 mark

5. Write an 8086 assembly language program to add 05 and 03. Store the result in the BH register.
   Data loading : 1 mark

Addition : 1 mark

Storing the result : 1 mark

6. What is ISR? Differentiate ISR and normal subroutine.

ISR : 1.5 marks

Subroutine : 1.5 marks

Part B

7. Explain the physical memory organization of 8086 with a neat diagram. How does the 8086 processor accesses a word from an odd memory location? How many memory cycles does it take?

Diagram : 3 marks

Explanation of memory access procedure : 2 marks

Explanation of odd memory location access procedure : 2 marks

8. How does the physical address calculated in 8086? Calculate the physical address of an instruction. The contents of segment registers are given as CS:1050H , DS : 2005H, SS : 3005H and IP : 5550H.

Address calculation mechanism : 3 marks

Finding the correct segment register : 1 mark

Calculating the physical address : 3 mark

9. Draw and explain the architecture of the 8086 microprocessor.

Architecture of the 8086 : 4 Marks

Explanation : 3 Marks

10. Identify different methods to specify the operands in 8086 microprocessor instructions. Explain each of them.

Listing out addressing modes : 2 marks

Explanation with example : 5 marks

11. What are the various classifications of instructions based on the operations they perform?
    Explain each with examples.
    Listing out the groups : 4 marks
    Examples : 3 marks

12. Write an 8086 assembly language program for finding the sum of the squares of first N natural numbers. Value of N is stored at location 2500H. Result should be stored at location 2600H.
    Logic : 5 marks
    Using correct address : 2 marks
13. Explain the procedure to handle an Interrupt in 8086.
    Flow chart : 4 marks
    Explanation : 3 marks

Internal : 2

Semester:5-Scheme 2019 Date : 05/02/2022

Subject : MICROPROCESSORS AND MICROCONTROLLERS (CST307) Time : 09:30 - 11:30

Faculty : Ancy K Sunny Max Marks: 60

Part A
Answer all questions

Q.No			Max Marks	CO	BT/CL
1		*Illustrate the events performed in 8086 when an interrupt occurs. [PI 2.1.1]*	3	3	L2
2		*Identify the mode and Input - Output configurations for various ports in 8255, after its control register is loaded with 86H. [PI 4.1.3]*	3	4	L3
3		*Explain how the INT n instruction finds the starting address of its interrupt service routine. [PI 2.1.1]*	3	3	L2
4		*List the features of 8257, DMA controller. [PI 4.1.3]*	3	4	L1
5		*Describe the role of Interrupt Request Register and In-Service Register in 8259. [PI 2.1.1]*	3	4	L2
6		*How 8254 is used as a square wave generator? [PI 4.1.3]*	3	4	L3
Part B					
Answer any 6 questions					
Q.No			**Max Marks**	**CO**	**BT/CL**

7		*Design an interface between 8086 CPU and two chips of 16K x 8 EPROM and two chips of 32K x 8 RAM. Select the starting address of EPROM suitably. The RAM address must start at 00000H. [PI 2.1.3]*	7	4	L3
8		*Illustarte the different modes of 8254/8253 programmable timer using its relevant waveforms. [PI 4.1.3]*	7	4	L2
9		*With neat diagram describe the architecture of 8259. [PI 4.1.3]*	7	4	L2
10	a	*Explain the Mode 2 operation of 8255. [PI 2.1.1]*	4	4	L2
10	b	*What is meant by maskable and non-maskable interrupts? [PI 2.1.1]*	3	3	L1
11	a	*What is an Interrupt Vector Table (IVT)? Provide a diagrammatic representation of the IVT of 8086. [PI 2.1.1]*	3	3	L2

11	b	*Describe the five dedicated interrupts of 8086. [PI 4.1.3]*	4	3	L2
12		*Interface an 8255 chip with 8086 to work as an Input-Output port. Initialize port A as output port, Port B as input port and Port C as output port. Port B address should be 0842H. Write an ALP to sense switch positions SW0–SW7 connected at port B. The sensed pattern is to be displayed on port A, to which 8 LED's are connected, while port C lower displays the number of on switches out of the total eight switches. [PI 2.1.3]*	7	4	L3
13		*Explain the architecture of 8257 with diagram. [PI 4.1.3]*	7	4	L2
14		*Interface two 4K x 8 EPROMS and two 4K x 8 RAM chips with 8086. Select suitable maps. [PI 2.1.3]*	7	4	L3

Evaluation

USN	Name	Present (P) / Absent (Ab)	Q11		Q12	Q13	Q14	Q1	Q2	Q3	Q4	Q5		Q6	Q7	Q8		Q9	Q10		IA Total	BT/CL
												a		b	a	b						
LVML19CS116	Anurag A M	P	2	0	0	0	0	0	2	0	2	0		0	0	0		0	0	0	6	Understand

USN	Name	Present (P) / Absent (Ab)	Q11		Q12	Q13	Q14	Q1	Q2	Q3	Q4	Q5	Q6	Q7	Q8	Q9	Q10		IA Total	BT/CL
												a	b	a	b					
LVML19CS117	Aromal Prakash K V	P	0	0	0	0.5	1	0	0	0	5	0	0	1.5	1	0	3	1	13	Understand
LVML19CS118	Kiran P P	P	2	0	1	2	1.5	1.5	0	0	4	2	3	0	2	0	5	0	24	Apply
VML19CS002	Abhijai K	P	1.5	0	0.5	0	1	0.5	0	3	6	3	0.5	0	0	0	3	0	19	Understand
VML19CS005	Achal Dev P	P	2.5	0.5	1.5	2	2	1.5	0	2	6.5	1.5	2.5	1	0	0	3	0	26.5	Apply
VML19CS007	Adila Farha P K	P	2	1	1	1.5	1	1	0	1	4	0	2	2	0	0	6	0	22.5	Understand
VML19CS008	Adithya T K	P	3	2	2	2	3	1	0	4	6.5	3	2	2	3	0	6	5	44.5	Apply
VML19CS010	Adwaid Sahadevan M	P	0.5	0	0	0.5	0	0	0	2.5	0	0	0	2	0	0	0	0	5.5	Understand
VML19CS012	Adwetha Falgunan	P	2.5	0	2	2	2	0.5	0	6	6.5	1	2	0.5	3	0	6	0	34	Understand
VML19CS015	Akhil Kumar K	P	1.5	0	0.5	0	1	0	0	0	6	1.5	2	2	3	0	4.5	0	22	Understand
VML19CS017	Akshay Jayachandran V V	P	1	0.5	0.5	1.5	1.5	0	0	1	4	2	2	0	0	0	3.5	0	17.5	Understand
VML19CS019	Alan Saji	P	2.5	0	2.5	0	2	0	0	5	5.5	2	3	1	3	0	0	0	26.5	Understand
VML19CS021	Aleena Mathews	P	3	3	2	3	3	3	7	7	6.5	2	3	0	0	0	6	5	53.5	Apply
VML19CS023	Alisha Mathew	P	2.5	0.5	0.5	1.5	0	0	0	0	4	2	2	0	0	0	0	4.5	17.5	Apply
VML19CS024	Amalraj P	P	3	2	1.5	2	2	0	0	3.5	6	3	2.5	1.5	0.5	0	4	0	31.5	Apply
VML19CS026	Anagha P P	P	0.5	1	3	2	2.5	2	0	3	6.5	1.5	2	2.5	1.5	0	6.5	0	34.5	Apply
VML19CS028	Aneesha S	P	3	1.5	0	2.5	0	0.5	2	1.5	0	0.5	2	0	0	0	3	6	22.5	Apply
VML19CS030	Anjima Govindan	P	0	1	0.5	0.5	0.5	0	0	0.5	0	0.5	2	0	1	0	1	4	11.5	Apply
VML19CS031	Annapoorna K K	P	0	0	0	2	0.5	0	0	0.5	6	2	1	0	0	0	6.5	0	18.5	Understand
VML19CS035	Anupama K V	P	0	0	0	1.5	1.5	0	0	3	6.5	3	2	0	0	2	0	0	19.5	Understand
VML19CS037	Anusree Venu	P	0.5	0	0	2	0	0	2	0	0	0	1.5	0	0	0	0	4.5	10.5	Apply
VML19CS039	Arya Sajiv	P	1.5	0.5	0	0	0	0	0	0	4.5	1	1	2	1	0.5	2	0	14	Understand
VML19CS040	Ashwin S Nambiar	P	2	3	2.5	2	2	0	0	0	6.5	3	2	3	1	0	6.5	0	33.5	Apply
VML19CS042	Athira Das	P	0	0	3	0	0	0	0	0.5	0	1	0	0	0	0	2	0	6.5	Understand
VML19CS044	Aysha Nahadha	P	3	0.5	0.5	2	1.5	2	6	4	6.5	3.5	3	0	0	0	3	5	40.5	Apply
VML19CS046	Darsan Dinesh	P	1	1	1	1	1.5	0.5	0	0	6	0.5	1	0	0	0	2	0	15.5	Understand
VML19CS048	Deekshith K K	P	2	3	1.5	0.5	2	2.5	0	2	6.5	1.5	2.5	2	2.5	0	6	0	34.5	Apply
VML19CS050	Devika C	P	0.5	2.5	0.5	1.5	0	0	0	1	4	2	2	0	0	0	4.5	0	18.5	Apply
VML19CS052	Diya P	P	2.5	2.5	1.5	3	2.5	2	0	6	0	3	2.5	0	0	7	0	1	33.5	Apply
VML19CS054	Don Mariya	P	0	2	0	0.5	1	0	4.5	1.5	4.5	0.5	2	2	0	0	3	0	21.5	Apply
VML19CS055	Eaby Thomas C	P	1.5	0	0	0.5	1.5	0	0	0	4	0.5	2	0	0	0	0	1	11	Understand
VML19CS058	Farisa K P	P	3	0.5	1	0	2	0	7	0	6.5	1.5	2	2.5	0	0	6	2	34	Apply
VML19CS061	Harold Prakash	P	2	0	0.5	0.5	0	0	0	0	4	0	1	0	0.5	0	0	0	8.5	Understand
VML19CS063	Janvin Joseph	P	3	2	1	2	2.5	1.5	0	1	6.5	1.5	2	1	0.5	0	5	0.5	30	Apply
VML19CS065	Jithin Jose	P	2	0	2	2	1.5	0	0	0	6	0.5	3	2.5	3.5	0	6	2	31	Understand
VML19CS067	Kavya Pushpan	P	2	1	0.5	2.5	0	1	0	5	5.5	0	3	2.5	3.5	0	6.5	0	33	Understand
VML19CS069	Kiran Valsalan Nair	P	1	0.5	0.5	2	0	0	0	1	4	2	2.5	1	1	0	6	0	21.5	Understand

USN	Name	Present (P) / Absent (Ab)	Q11		Q12	Q13	Q14	Q1	Q2	Q3	Q4	Q5	Q6	Q7	Q8	Q9	Q10		IA Total	BT/CL
												a	b	a	b					
VML19CS071	Mary Joy	P	2.5	1	0.5	1.5	1.5	0.5	1.5	1	6.5	2	3	2	2	0	3	1	28.5	Understand
VML19CS074	Nathasha K V	P	1.5	2	2	1.5	2.5	1	0	0	6.5	0.5	2.5	3	2	0	5	2	32	Apply
VML19CS075	Nihal O	P	2.5	0.5	0	1	1.5	0.5	0	2	6.5	0	2	2	0	0	3.5	0	22	Understand
VML19CS078	Nirmal Shaju	P	0.5	1	0	2	1	0	0	2	4.5	0.5	2	0	1	0	5.5	0	20	Understand
VML19CS080	Puliyile Kandi Muhammed Jassim	P	3	0	0.5	0	2.5	0	0	0	6	2.5	3	0	3	0	0	0	20.5	Understand
VML19CS081	PV Gayathri	P	0	2	0	0.5	0	0.5	0	1	0	1	0	0	0	0	2	0	7	Apply
VML19CS083	Ranjul Arumadi	P	2	1.5	1	2.5	0.5	0	0	1	3	0.5	2.5	1	0	0	4.5	0	20	Apply
VML19CS084	Rhea Renjith	P	2.5	0.5	0	2	2.5	0.5	5	4	4.5	2	2	0	0	0	6	6	37.5	Apply
VML19CS086	Roby K S	P	1	0	0	1.5	1.5	0	0	0	5.5	0	3	0	1.5	0	5.5	0	19.5	Understand
VML19CS089	Sanand Chandran	P	1.5	0.5	0	1	0	0.5	0	0	4	1	2	1	2	0	3.5	0	17	Understand
VML19CS091	Sanjuktha Sanjay	P	2.5	2	2	2.5	3	3	0	4	6.5	3.5	3	0	0	0	6	0	38	Apply
VML19CS094	Sharanya Ullas	P	2.5	3	0.5	2.5	3	1.5	0	1.5	6.5	2	2	0	0	0	4.5	0	29.5	Apply
VML19CS096	Shijas P	P	1.5	0	0	1	0	0	0	0	1	0.5	2	0	0.5	0	3	1	10.5	Understand
VML19CS098	Shradha Sujith	P	3	1	2	2	2	0	0	0	6.5	3	2	1	0.5	0	0	0	23	Understand
VML19CS101	Sidharth A S	P	0.5	1.5	0	1	0.5	0	0	1	6	2	1.5	0	0.5	0	4	1	19.5	Apply
VML19CS103	Sidharth Suresh Nambiar	P	2	0	0	0	1	0	0	0	5.5	2.5	3	2	2	0	0	0	18	Understand
VML19CS104	Sneha Anil	P	2.5	2	2	2.5	2.5	3	1	6	6	3	2	1	0	5	0	0	38.5	Apply
VML19CS106	Sona Jose	P	2.5	0	2	2	1.5	2	0	0	6	3	2.5	3	4	0	6	1	35.5	Apply
VML19CS108	Sooraj Mohan	P	2.5	0	0	0	0	0	0	0	0	0	3	0	0	0	0	0	5.5	Understand
VML19CS110	Sreevedh Hareesh	P	1	2	0	2	1	1	0	3	3	1.5	1	0	0	0	6	0	21.5	Apply
VML19CS112	Theerth M	P	0.5	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0.5	No Level
VML19CS114	Vismaya Vinoth Kumar	P	2.5	0	0.5	2	2	1	0	2.5	4	0	1.5	1	2.5	0	5	0	24.5	Understand

2 Scheme of Evaluation

VIMAL JYOTHI ENGINEERING COLLEGE, KANNUR

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

SEMESTER 5

CST 307 MICROPROCESSORS AND MICROCONTROLLERS

SECOND INTERNAL EXAMINATION

ANSWER SCHEME

PART A

(ANSWER ALL QUESTIONS)

1. When a device raises an interrupt the processor first completes the execution of instruction.

   Moving values of PC and flags into Stack.

   Then it loads the Program Counter (PC) with the address of the first instruction of the ISR.

Explaining ISR execution with above mentioned points - 3 Marks

2. **Identifying as I/O command - 0.5 Marks**

   Port A mode and input or output - 1 Marks

   Port B mode and input or output - 1 Marks

   Port C input or output - 0.5 Marks

   Bit representation 1 0 0 0 0 1 1 0

   1 - I/O command

   0 0 - Port A in Mode 0

   0 - Port A as output port

   0 - Port C upper output port

1 - Port B in Mode 1

1 - Port B as input port

0 - Port C lower as output port

3. **Mentioning 4n - 2 Marks, example - 1 Marks**

   Each interrupt type is given a number between 0 to 255 and the address of each interrupt is found by multiplying the type by 4 e.g. for type 11, interrupt address is 11 x 4 = 4410= 0002CH

4. **Any 4 features - 3 Marks**
5. **Interrupt Request register - 1.5 Marks**

   In service Register - 1.5 Marks

6. **Identifying mode - 2 Marks, control word - 1 Mark**

PART B

(Answer Any 6 Questions)

7. **Memory Mapping - 3 Marks**

   Decoding Circuit - 2 Marks

   Interfacing Diagram - 2 Marks

8. **Different Modes explanation - 1 Marks**

   Timing Diagram - 1 Marks for each mode (total 6 modes)

9. **Architecture Diagram - 3 Marks**

   Explanation - 4 Marks

10. **A. Mode 2 explanation - 2 Marks, Timing Diagram - 2Marks**

    B. Maskable interrupts - 1.5 Marks, Non - maskable interrupts - 1.5 Marks

11. **A. IVT explanation - 2 Marks**

Table - 1 Mark

B. Listing 5 Interrupts - 1.5 Marks

Interrupt explanation - 2.5 Marks

12. Control word - 1.5 Marks

Port Address calculation - 1.5 Marks

ALP - 4 Marks

13. Architecture - 4 Marks

Explanation - 3 Marks

14. Memory Mapping - 3 Marks

Decoding Circuit - 2 Marks

Interfacing Diagram - 2 Marks

Internal : 3

Semester:5-Scheme 2019 Date : 18/02/2022

Subject : MICROPROCESSORS AND MICROCONTROLLERS (CST307) Time : 09:30 - 11:30

Faculty : Ancy K Sunny Max Marks: 60

Part A
Answer all questions

Q.No			Max Marks	CO	BT/CL
1		*Compare 8085 and 8086 microprocessors in terms of width of data bus and memory addressing capacity.* *[PI 3.3.1]*	3	1	L2
2		*8086 has CS,DS,ES and SS in its architecture. Identify its uses.* *[PI 3.1.6]*	3	1	L2
3		*8086 has a queue in its Bus Interface Unit. What is the size of that queue? Discuss the function of the instruction queue in 8086.* *[PI 1.3.1]*	3	1	L2
4		*What is an assembler? Discuss the use of assembler directive.* *[PI 1.3.1]*	3	2	L1
5		*Write an 8086 assembly language program to add 05 and 03. Store the result in the BH register.* *[PI 3.2.1]*	3	2	L3
6		*Compare microprocessor and microcontroller.* *[PI 3.3.1]*	3	5	L2
Part B					
Answer any 6 questions					

Q.No			Max Marks	CO	BT/CL
7		*Explain the physical memory organization of 8086 with a neat diagram. How does the 8086 processor access a word from an odd memory location? How many memory cycles does it take?* *[PI 1.3.1]*	7	1	L2
8		*How is the physical address calculated in 8086? Calculate the physical address of an instruction. The contents of segment registers are given as CS:1050H , DS : 2005H, SS : 3005H and IP : 5550H.* *[PI 1.3.1]*	7	1	L3
9		*Draw and explain the architecture of the 8086 microprocessor.* *[PI 1.3.1]*	7	1	L2
10		*Identify different methods to specify the operands in 8086 microprocessor instructions. Explain each of them.* *[PI 2.1.2]*	7	2	L3

11		*Draw and explain 8051 Architecture.* *[PI 1.3.1]*	7	5	L1
12		*Write an 8086 assembly language program for finding the sum of the squares of first N natural numbers. Value of N is stored at location 2500H and the result should be stored at location 2600H.* *[PI 2.1.2]*	7	2	L3
13		*Write an 8051 assembly language program to find the sum of two 2X2 matrices.* *[PI 2.1.2]*	7	3	L3
14		*What are the various classifications of instructions based on the operations they perform? Explain each with examples.* *[PI 1.3.1]*	7	2	L2

Evaluation

USN	Name	Present (P) / Absent (Ab)	Q11	Q12	Q13	Q14	Q1	Q2	Q3	Q4	Q5	Q6	Q7	Q8	Q9	Q10	IA Total	BT/CL
LVML19CS116	Anurag A M	P	1	1	0.5	0.5	0	0.5	0	0	0.5	2	0	0.5	2	0	8.5	No Level
LVML19CS117	Aromal Prakash K V	P	3	1	0	0.5	0	1	0	2	3.5	0	2	0	0	0	13	Understand
LVML19CS118	Kiran P P	P	0	1	1.5	2	2	1	3	0	4	3	3	0	0	4	24.5	Apply
VML19CS002	Abhijai K	P	3	2	0	0	0.5	0	0	0	6	0	4	0	0	0	15.5	Understand

USN	Name	Present (P) / Absent (Ab)	Q11	Q12	Q13	Q14	Q1	Q2	Q3	Q4	Q5	Q6	Q7	Q8	Q9	Q10	IA Total	BT/CL
VML19CS005	Achal Dev P	P	3	2.5	2	2	2	2.5	0	4	6.5	3	1	0.5	0	5	34	Apply
VML19CS007	Adila Farha P K	P	1	0.5	2	1.5	1	2	4.5	0	6.5	3	2	0	0	0	24	Understand
VML19CS008	Adithya T K	P	3	2	1	1.5	0	0	5	0	6.5	5.5	3.5	0	3	0	31	Apply
VML19CS010	Adwaid Sahadevan M	P	0	0.5	0.5	1	1.5	0	1.5	0	0	0	0	0	0	0	5	Apply
VML19CS012	Adwetha Falgunan	P	0	1	0.5	2	2	0.5	3	0	5.5	0	5	0	0	0	19.5	Apply
VML19CS015	Akhil Kumar K	P	1.5	0	0	2	2	0	0	0	5.5	0	3	0	0	3.5	17.5	Apply
VML19CS017	Akshay Jayachandran V V	P	3	1	0	2	2	0	0	0	4	5	3	0	0	4.5	24.5	Apply
VML19CS019	Alan Saji	P	1	2	1	0	2.5	2	0	6	6.5	4	0	0	0	1	26	Apply
VML19CS021	Aleena Mathews	P	3	3	3	3	3	1.5	6	2	6.5	6.5	4.5	0	0	0	42	Apply
VML19CS023	Alisha Mathew	P	0	1	1	1.5	0	0	3	0	5.5	0	4.5	0	0	0	16.5	Understand
VML19CS024	Amalraj P	P	3	2	2.5	2.5	3	2.5	1.5	0	6.5	4.5	5	0	0	0	33	Apply
VML19CS026	Anagha P P	P	3	3	2	2.5	1.5	0	5.5	5	6.5	4	6	0	0	2.5	41.5	Apply
VML19CS028	Aneesha S	P	3	1.5	2.5	3	3	0	5	0.5	4	6.5	0	7	0	3	39	Apply
VML19CS030	Anjima Govindan	P	2.5	2	2	1.5	0	2	0	2	5	4.5	5.5	0	0	2	29	Apply
VML19CS031	Annapoorna K K	P	3	1.5	0	1.5	0	0	4	0	5	5	1	0	0	0.5	21.5	Apply
VML19CS035	Anupama K V	P	3	2	2	1.5	2	0	4.5	0	6.5	5	3	0	0	0	29.5	Apply
VML19CS037	Anusree Venu	P	3	3	2	1.5	0.5	0	4	1	6	0	0	0	0	0	21	Understand
VML19CS039	Arya Sajiv	P	3	1	1.5	0.5	0	1	1	0	5	1.5	5.5	0	0	5	25	Understand
VML19CS040	Ashwin S Nambiar	P	3	1.5	2	3	3	2	5	0	6.5	6.5	5	1	0	4.5	43	Apply
VML19CS042	Athira Das	P	1.5	1	0	1.5	1	1	0	2	5.5	2	1.5	1	0	0	18	Understand
VML19CS044	Aysha Nahadha	P	3	2.5	0.5	2	2.5	0	5	0	6	4.5	6	0	0	6	38	Apply
VML19CS046	Darsan Dinesh	P	0	1	1	1.5	2	0	0	0	0	0	0	0	0	0	5.5	Apply
VML19CS048	Deekshith K K	P	3	3	2	2	2.5	2	0	6	6.5	0	0.5	6	6	0	39.5	Apply
VML19CS050	Devika C	P	3	2	3	2.5	2	2	0	0.5	6.5	4	0	5	0	3	33.5	Apply
VML19CS052	Diya P	P	3	3	2.5	2.5	2	2.5	0	0	4.5	6	3	1	5	0	35	Apply
VML19CS054	Don Mariya	P	3	1.5	1.5	1	0	0.5	4.5	0	5.5	3	5.5	0	0	0	26	Understand
VML19CS055	Eaby Thomas C	P	0	2.5	0	2	3	0	3	0	5	0	0	3	0	1	19.5	Apply
VML19CS058	Farisa K P	P	3	2	2.5	3	2	0.5	3	2	6.5	6	4.5	0	0	4	39	Apply
VML19CS061	Harold Prakash	P	0	1	0.5	1	3	0	0.5	0	4	0	0	4	0	5.5	19.5	Apply
VML19CS063	Janvin Joseph	P	3	2	0	2	1	1	5	2	5.5	5	0	0.5	0	3.5	30.5	Apply
VML19CS065	Jithin Jose	P	3	1.5	2.5	2.5	2.5	2	5	6.5	6	5	0	0	0	0	36.5	Apply
VML19CS067	Kavya Pushpan	P	3	2	3	2	3	1	2.5	3	6	6	4	2	0	0	37.5	Apply
VML19CS069	Kiran Valsalan Nair	P	3	1	0.5	1	3	1	3	1	4	4	0	0	0	4	25.5	Apply
VML19CS071	Mary Joy	P	3	2.5	2	2	3	0	2	1	6.5	6	0	1.5	0	4	33.5	Apply
VML19CS074	Nathasha K V	P	3	2	1	2.5	3	0	5	2	6	3.5	1	1.5	0	2	31.5	Apply
VML19CS075	Nihal O	P	3	2	2	2.5	2	1.5	5.5	1	6.5	6.5	3.5	0	0	0	36	Apply

USN	Name	Present (P) / Absent (Ab)	Q11	Q12	Q13	Q14	Q1	Q2	Q3	Q4	Q5	Q6	Q7	Q8	Q9	Q10	IA Total	BT/CL
VML19CS078	Nirmal Shaju	P	3	1	1.5	1.5	1.5	2	3	1	4.5	0	3	0	0	3	25	Apply
VML19CS080	Puliyile Kandi Muhammed Jassim	P	2.5	1.5	2	1.5	0	2	0	0	5	0	0	0	1	4	19.5	Understand
VML19CS081	PV Gayathri	P	1	2.5	3	2	2	0	4	1.5	6	0	0	0	0	1.5	23.5	Apply
VML19CS083	Ranjul Arumadi	P	2.5	3	1.5	2.5	3	2	5.5	1	6	5.5	3	4	4	6	45.5	Apply
VML19CS084	Rhea Renjith	P	3	1.5	2	2.5	2.5	1.5	5	3	6	5	5	0	0	4	41	Apply
VML19CS086	Roby K S	P	2	2	0	2	1	0.5	2	1.5	4	0	1	0	0	3	19	Understand
VML19CS089	Sanand Chandran	P	3	1.5	2.5	1.5	3	1	0	0	4	0	0	0	0	4.5	21	Apply
VML19CS091	Sanjuktha Sanjay	P	2	1.5	2.5	2	3	2	0	1	6	6	2	0.5	0	3.5	32	Apply
VML19CS094	Sharanya Ullas	P	3	2.5	2.5	2.5	2	2	4	3	4.5	6	3	0	0	0	35	Apply
VML19CS096	Shijas P	P	3	2.5	0	2	0	2	2.5	2	2.5	3.5	0	0.5	0	2	22.5	Apply
VML19CS098	Shradha Sujith	P	3	1.5	2.5	2.5	0	2.5	0	0	6	1	0	0	0	5	24	Understand
VML19CS101	Sidharth A S	P	3	2.5	2.5	2	2	1.5	0	2	6	5	0	1.5	0	5	33	Apply
VML19CS103	Sidharth Suresh Nambiar	P	3	1	1.5	0.5	0	0.5	1	1	5	0	4.5	0	0	0	18	Understand
VML19CS104	Sneha Anil	P	3	2	2.5	2.5	2	2	4.5	0	5	6.5	0	1	0	3	34	Apply
VML19CS106	Sona Jose	P	3	3	2	2.5	3	2	4.5	5	6	6	0	0	0	5	42	Apply
VML19CS108	Sooraj Mohan	P	2	0	0	1.5	0	0	0	0	0	0	0	1	0	0	4.5	Understand
VML19CS110	Sreevedh Hareesh	P	1	1	2	2	2	0.5	3	0	6	6	4.5	0	5	4.5	37.5	Apply
VML19CS112	Theerth M	P	2	1.5	1	0	0	1	0	0	0	0	0	0	0	0	5.5	Understand
VML19CS114	Vismaya Vinoth Kumar	P	2	1	0	1.5	0	1	1	0	5	2	2	0	0	2	17.5	Understand

2 Scheme of Evaluation

VIMAL JYOTHI ENGINEERING COLLEGE, KANNUR

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

SEMESTER 5

CST 307 - Microprocessors and Microcontrollers

Internal Assessment 3

Scheme for valuation

Part A

1. Compare 8085 and 8086 microprocessors in terms of width of data bus and memory addressing capacity.
   1.5 mark for each microprocessor
2. 8086 has CS,DS,ES and SS in its architecture. Identify its uses.
   CS : 1 mark
   DS : 1 mark
   SS : 0.5 mark
   ES : 0.5 mark

3. 8086 has a queue in its Bus Interface Unit. What is the size of that queue? Discuss the function of the instruction queue in 8086.
   Size : 1 mark
   Use : 2 marks

4. What is an assembler? Discuss the use of assembler directive.
   Assembler : 2 marks
   Assembler directives : 1 mark

5. Write an 8086 assembly language program to add 05 and 03. Store the result in the BH register.
   Data loading : 1 mark

Addition : 1 mark

Storing the result : 1 mark

6. Compare microprocessor and microcontroller.
microprocessor : 1.5 marks

microcontroller : 1.5 marks

Part B

7. Explain the physical memory organization of 8086 with a neat diagram. How does the 8086 processor accesses a word from an odd memory location? How many memory cycles does it take?

   Diagram : 3 marks

   Explanation of memory access procedure : 2 marks

   Explanation of odd memory location access procedure : 2 marks

8. How does the physical address calculated in 8086? Calculate the physical address of an instruction. The contents of segment registers are given as CS:1050H , DS : 2005H, SS : 3005H and IP : 5550H.

   Address calculation mechanism : 3 marks

   Finding the correct segment register : 1 mark

   Calculating the physical address : 3 mark

9. Draw and explain the architecture of the 8086 microprocessor.

   Architecture of the 8086 : 4 Marks

   Explanation : 3 Marks

10. Identify different methods to specify the operands in 8086 microprocessor instructions. Explain each of them.

    Listing out addressing modes : 2 marks

    Explanation with example : 5 marks

11. Draw and explain 8051 Architecture.

Architecture of the 8051 : 4 Marks

Explanation : 3 Marks

12. Write an 8086 assembly language program for finding the sum of the squares of first N natural numbers. Value of N is stored at location 2500H. Result should be stored at location 2600H.

Logic : 5 marks

Using correct address : 2 marks

13. Write an 8051 assembly language program to find the sum of two 2X2 matrices.

Logic : 5 marks

Using correct Registers : 2 marks

14. What are the various classifications of instructions based on the operations they perform? Explain each with examples.

Listing out the groups : 4 marks

Examples : 3 marks

CALENDAR FOR ACADEMIC E-AUDIT 2020 – 21

Audit No	Period of Audit	Last Date for Submission of Report	Last Date for response of the Principal
1	27/10/20 to 04/11/20	10/11/20	13/11/20
2	28/12/20 to 31/12/20	08/01/21	14/01/21

Vimal Jyothi
Engineering College Chemperi

Vidhya S. S CSE <vidhyasud@vjec.ac.in>

Fwd: Second Internal academic audit schedule

2 messages

Dr Benny Joseph <bennyjoseph@vjec.ac.in> Fri, Jan 29, 2021 at 9:58 PM
Reply-To: staff@vjec.ac.in
To: staff@vjec.ac.in

---------- Forwarded message ---------
From: **Dr. D Anto Sahayadhas ECE** <dr.anto@vjec.ac.in>
Date: Fri, 29 Jan, 2021, 6:49 pm
Subject: Second Internal academic audit schedule
To: Dr Benny Joseph <bennyjoseph@vjec.ac.in>

Dear Sir,
Herewith I had attached the schedule for the second internal academic audit. Kindly do the needful.

With regards,
Dr.D.Anto Sahaya Dhas M.E, Ph.D.,
Professor & Head,
Department of Electronics & Communication Engineering,
Vimal Jyothi Engineering College,
Chemperi, Kannur.

Mobile: **+91-9486747931, 8190847931**

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You received this message because you are subscribed to the Google Groups "STAFF CSE" group.
To unsubscribe from this group and stop receiving emails from it, send an email to staffcs+unsubscribe@vjec.ac.in.
To view this discussion on the web visit https://groups.google.com/a/vjec.ac.in/d/msgid/staffcs/CAFu%3DrXET%3D7%3Dq%2BztU1PC5jCrXhED8K_t0CKLLMFj-NqLEH6OOuA%40mail.gmail.com.

2nd e-audit schedule AY2020-21.pdf
68K

Dr. Jeethu V. Devasia CSE <jeethuthomas@vjec.ac.in> Mon, Feb 1, 2021 at 9:36 AM
To: fcs@vjec.ac.in

For reference.

Best Regards,

Dr. Jeethu V. Devasia
Associate Professor and Head
Department of Computer Science and Engineering
Vimal Jyothi Engineering College
Kannur, Kerala - 670 632

---------- Forwarded message ---------
From: **Dr Benny Joseph** <bennyjoseph@vjec.ac.in>
Date: Sat, Jan 30, 2021 at 10:37 PM
Subject: Fwd: Second Internal academic audit schedule
To: Biju Mathew <bijupmathews@vjec.ac.in>, Dr. Anto EC <dr.anto@vjec.ac.in>, Dr. Glan Devadhas <glandeva@vjec.ac.in>, Dr. Sampath EI <vsampath@vjec.ac.in>, Dr. TD John <drtdjohn@vjec.ac.in>, Jagannath.M.P <jagannathmp@vjec.ac.in>, Jeethu CS <jeethuthomas@vjec.ac.in>, Laly James <lalyjames@vjec.ac.in>, Raju Kk

<rajukk@vjec.ac.in>, Roshini T.V <roshini.tv@vjec.ac.in>, Assistant manager <asstmanager@vjec.ac.in>, Bursar <bursar@vjec.ac.in>, Chairman <chairman@vjec.ac.in>, Fr Jinu <jinuachan@vjec.ac.in>, Fr. Jins <santhome@vjec.ac.in>, K.J.Sebastian Puthenpura <sebastianputhenpura@vjec.ac.in>, Manager <manager@vjec.ac.in>, Office VJEC <office@vjec.ac.in>, P.K Mathew <mathewpunnackal@vjec.ac.in>, Wardens <warden@vjec.ac.in>

Regards
Benny Joseph

---------- Forwarded message ---------
From: **Dr.Biju Mathew** <bijupmathews@vjec.ac.in>
Date: Sat, 30 Jan 2021 at 11:39
Subject: Re: Second Internal academic audit schedule
To: Dr. Benny Joseph <bennyjoseph@vjec.ac.in>

The following criteria are verified during internal audit as per Ktu E - audit manual

DEPARTMENT SPECIFIC DOCUMENTS

Hod (Department Coordinator)

- Admission list/ student list both UG and PG
- Details of faculty and staff with qualification
- The functioning of department-level IQAC
- The facility of Departmental library with respect to volume, the title of books, online & print journals
- Department/college calendar and compliance to the Academic Calendar of KTU
- Result analysis of previous odd semester

Class advisor both UG And PG

- Name list of active students
- List of students offered Honours/ minors
- the details of UG/PG students undergoing/undergone MOOC courses with the percentage of students
- Academic calendar with days earmarked with working days, holidays, other activities etc
- Agenda, Minutes and action taken reports of course/class committees of UG/PG (2015 regulation)
- Agenda, Minutes and action taken reports of advisory meetings (2019 regulation)
- Internal RA
- **Result Analysis (UG/PG) Semester wise and list of students without backlog and CGPA of the corresponding student.**
- Details of faculty evaluation and action taken on it- (i) number of feedbacks taken in a semester (ii) % of students participated (iii) corrective actions taken for improving teaching effectiveness (iv) mode and criteria for evaluation etc.
- Register showing activity points - 2017, 2018,& 2019 and copy of uploaded activity-wise and student wise consolidated
- Details of NPTEL/SWAYAM course materials recommended for reading

Faculty wise

- Course Diary for all the courses- as per KTU directions

Tutorial details if any

- Series test question paper and sample answer scripts

other assessment - assignment details

- Laboratory manual, demonstration video, simulation set up etc.
- Rubrics for continuous evaluation
- Periodic assessment details for all courses including laboratory, project
- Project (Mini project/Design project/Final semester project) progress review reports

Result Analysis of course - list of students and criteria for sleeting booster / remedial class

- Details of faculty evaluation and action taken on it if any
- Feedback of students on online class.
- Conduct of PG thesis work

With warm regards,

Dr.Biju Mathew, M.Tech, MBA, PGDEPMA, MSc, Ph.D.
Chartered Engineer, Psychologist , Life Trainer
Dean (Exams), Professor & HOD
Department of Civil Engineering
Vimal Jyothi Engineering College
Chemperi- Kannur- Kerala
Mob: 09847436426, 08281 531 426
Admissions 2020 VJEC: http://vjec.ac.in/admission/admission-enquiry/
http://vjec.ac.in/admission/b-tech-admission/

[Quoted text hidden]

APJ Abdul Kalam Technological University
CET Campus, Thiruvananthapuram
Kerala -695016
India

Academic Audit Report
2021 - 2022

Basic Details

Institution	**VIMALJYOTHI ENGINEERING COLLEGE**
First Auditor Name	**Aftab A Karim**
Second Auditor Name	**Aseem K**
Visit	**First**
Semester Type	**Odd**

College Specific Assessments

Key Aspects	Rating	Auditor Remarks	Principal's Response
Compliance to the Academic Calendar of KTU (A)	Excellent(5)		
Functioning of students grievances and appeal committee (A)	Good(4)		
Functioning of Academic Discipline Welfare committee (A)	Good(4)		
Average student to faculty ratio (A)	Fair(3)		
Faculty Retention (A)	Good(4)		
Faculty Qualification Index (A)	Fair(3)		
Number of qualified technical staff (A)	Good(4)		

Facility of central library with respect to volume and title of books, online print journals (A)	Good(4)		
Functioning of IQAC and Progress of IQAC report uploading (A)	Good(4)		
Institution Budget (A)	Good(4)		
Details of central computing facility (A)	Excellent(5)		
Principals response on previous audit reports (A)	Excellent(5)		

Student Interaction

Key Aspects	Rating	Auditor Remarks	Principal's Response
Syllabus coverage (B)	Excellent(5)		
Lab Facility and conduct of Labs (B)	Excellent(5)		
Co-curricular facility (B)	Good(4)		
Extra-curricular facility (B)	Good(4)		
Transferring all intimation from University in time (B)	Excellent(5)		
Approach of the management in addressing student grievance (B)	Good(4)		
Availability of Faculty for all subjects (A)	Excellent(5)		
Any other remarks (C)	Fair(3)		
Extra fees/fine imposed on students (B)	Fair(3)		

B.Tech

No of B.Tech Students	1610

ELECTRONICS & COMMUNICATION ENGG-(Full Time)

Key Aspects	Rating	Auditor Remarks	Principal's Response
Class/course committee meetings and action taken report (B)	Excellent(5)		
Advisory meetings and action taken report (B)	Excellent(5)		
Syllabus coverage as per course plan (B)	Excellent(5)		
Conduct of Tutorial Classes (B)	Excellent(5)		
Evaluation of Answer Scripts (A):	Excellent(5)		
Conduct of practical courses (B)	Excellent(5)		
Evaluation of students performance in practical classes (A):	Excellent(5)		
Facility to do experiments in the lab (A)	Excellent(5)		
Conduct of remedial/minor/honours classes (B)	Excellent(5)		
Maintenance of course diary (A)	Excellent(5)		
Assessment of Outcomes (A)	Excellent(5)		
Faculty evaluation amp remarks of the HoD (A)	Excellent(5)		
Conduct of Seminar, Mini project, Project amp Thesis (A)	Excellent(5)		
Computing facility in the department (A)	Excellent(5)		
Facility for co-curricular and extracurricular activities (B)	Good(4)		

Students attending MOOC or other online courses (B)	Excellent(5)		

Computer Science and Design-(Full Time)

Key Aspects	Rating	Auditor Remarks	Principal's Response
Class/course committee meetings and action taken report (B)	Excellent(5)		
Advisory meetings and action taken report (B)	Excellent(5)		
Syllabus coverage as per course plan (B)	Excellent(5)		
Conduct of Tutorial Classes (B)	Excellent(5)		
Evaluation of Answer Scripts (A):	Excellent(5)		
Conduct of practical courses (B)	Excellent(5)		
Evaluation of students performance in practical classes (A):	Excellent(5)		
Facility to do experiments in the lab (A)	Excellent(5)		
Conduct of remedial/minor/honours classes (B)	Excellent(5)		
Maintenance of course diary (A)	Excellent(5)		
Assessment of Outcomes (A)	Excellent(5)		
Faculty evaluation amp remarks of the HoD (A)	Excellent(5)		
Conduct of Seminar, Mini project, Project amp Thesis (A)	Excellent(5)		
Computing facility in the department (A)	Excellent(5)		

Facility for co-curricular and extracurricular activities (B)	Fair(3)		
Students attending MOOC or other online courses (B)	Excellent(5)		

MECHANICAL ENGINEERING-(Full Time)

Key Aspects	Rating	Auditor Remarks	Principal's Response
Class/course committee meetings and action taken report (B)	Excellent(5)		
Advisory meetings and action taken report (B)	Excellent(5)		
Syllabus coverage as per course plan (B)	Excellent(5)		
Conduct of Tutorial Classes (B)	Excellent(5)		
Evaluation of Answer Scripts (A):	Excellent(5)		
Conduct of practical courses (B)	Excellent(5)		
Evaluation of students performance in practical classes (A):	Excellent(5)		
Facility to do experiments in the lab (A)	Excellent(5)		
Conduct of remedial/minor/honours classes (B)	Excellent(5)		
Maintenance of course diary (A)	Excellent(5)		
Assessment of Outcomes (A)	Excellent(5)		
Faculty evaluation amp remarks of the HoD (A)	Excellent(5)		
Conduct of Seminar, Mini project, Project amp Thesis (A)	Excellent(5)		

Computing facility in the department (A)	Excellent(5)		
Facility for co-curricular and extracurricular activities (B)	Fair(3)		
Students attending MOOC or other online courses (B)	Excellent(5)		

CIVIL ENGINEERING-(Full Time)

Key Aspects	Rating	Auditor Remarks	Principal's Response
Class/course committee meetings and action taken report (B)	Excellent(5)		
Advisory meetings and action taken report (B)	Excellent(5)		
Syllabus coverage as per course plan (B)	Excellent(5)		
Conduct of Tutorial Classes (B)	Excellent(5)		
Evaluation of Answer Scripts (A):	Excellent(5)		
Conduct of practical courses (B)	Excellent(5)		
Evaluation of students performance in practical classes (A):	Excellent(5)		
Facility to do experiments in the lab (A)	Excellent(5)		
Conduct of remedial/minor/honours classes (B)	Excellent(5)		
Maintenance of course diary (A)	Excellent(5)		
Assessment of Outcomes (A)	Excellent(5)		
Faculty evaluation amp remarks of the HoD (A)	Excellent(5)		

Conduct of Seminar, Mini project, Project amp Thesis (A)	Excellent(5)		
Computing facility in the department (A)	Excellent(5)		
Facility for co-curricular and extracurricular activities (B)	Fair(3)		
Students attending MOOC or other online courses (B)	Excellent(5)		

ELECTRICAL AND ELECTRONICS ENGINEERING-(Full Time)

Key Aspects	Rating	Auditor Remarks	Principal's Response
Class/course committee meetings and action taken report (B)	Excellent(5)		
Advisory meetings and action taken report (B)	Excellent(5)		
Syllabus coverage as per course plan (B)	Excellent(5)		
Conduct of Tutorial Classes (B)	Excellent(5)		
Evaluation of Answer Scripts (A):	Excellent(5)		
Conduct of practical courses (B)	Excellent(5)		
Evaluation of students performance in practical classes (A):	Excellent(5)		
Facility to do experiments in the lab (A)	Excellent(5)		
Conduct of remedial/minor/honours classes (B)	Excellent(5)		
Maintenance of course diary (A)	Excellent(5)		
Assessment of Outcomes (A)	Excellent(5)		

Faculty evaluation amp remarks of the HoD (A)	Excellent(5)		
Conduct of Seminar, Mini project, Project amp Thesis (A)	Excellent(5)		
Computing facility in the department (A)	Excellent(5)		
Facility for co-curricular and extracurricular activities (B)	Fair(3)		
Students attending MOOC or other online courses (B)	Excellent(5)		

Artificial Intelligence and Data Science-(Full Time)

Key Aspects	Rating	Auditor Remarks	Principal's Response
Class/course committee meetings and action taken report (B)	Excellent(5)		
Advisory meetings and action taken report (B)	Excellent(5)		
Syllabus coverage as per course plan (B)	Excellent(5)		
Conduct of Tutorial Classes (B)	Good(4)		
Evaluation of Answer Scripts (A):	Excellent(5)		
Conduct of practical courses (B)	Excellent(5)		
Evaluation of students performance in practical classes (A):	Excellent(5)		
Facility to do experiments in the lab (A)	Excellent(5)		
Conduct of remedial/minor/honours classes (B)	Excellent(5)		
Maintenance of course diary (A)	Excellent(5)		

Assessment of Outcomes (A)	Excellent(5)		
Faculty evaluation amp remarks of the HoD (A)	Excellent(5)		
Conduct of Seminar, Mini project, Project amp Thesis (A)	Excellent(5)		
Computing facility in the department (A)	Excellent(5)		
Facility for co-curricular and extracurricular activities (B)	Fair(3)		
Students attending MOOC or other online courses (B)	Excellent(5)		

APPLIED ELECTRONICS & INSTRUMENTATION ENGINEERING-(Full Time)

Key Aspects	Rating	Auditor Remarks	Principal's Response
Class/course committee meetings and action taken report (B)	Excellent(5)		
Advisory meetings and action taken report (B)	Excellent(5)		
Syllabus coverage as per course plan (B)	Excellent(5)		
Conduct of Tutorial Classes (B)	Good(4)		
Evaluation of Answer Scripts (A):	Excellent(5)		
Conduct of practical courses (B)	Excellent(5)		
Evaluation of students performance in practical classes (A):	Excellent(5)		
Facility to do experiments in the lab (A)	Excellent(5)		
Conduct of remedial/minor/honours classes (B)	Excellent(5)		

Maintenance of course diary (A)	Excellent(5)		
Assessment of Outcomes (A)	Excellent(5)		
Faculty evaluation amp remarks of the HoD (A)	Excellent(5)		
Conduct of Seminar, Mini project, Project amp Thesis (A)	Excellent(5)		
Computing facility in the department (A)	Excellent(5)		
Facility for co-curricular and extracurricular activities (B)	Fair(3)		
Students attending MOOC or other online courses (B)	Excellent(5)		

COMPUTER SCIENCE & ENGINEERING-(Full Time)

Key Aspects	Rating	Auditor Remarks	Principal's Response
Class/course committee meetings and action taken report (B)	Excellent(5)		
Advisory meetings and action taken report (B)	Excellent(5)		
Syllabus coverage as per course plan (B)	Excellent(5)		
Conduct of Tutorial Classes (B)	Excellent(5)		
Evaluation of Answer Scripts (A):	Excellent(5)		
Conduct of practical courses (B)	Excellent(5)		
Evaluation of students performance in practical classes (A):	Excellent(5)		
Facility to do experiments in the lab (A)	Excellent(5)		

Conduct of remedial/minor/honours classes (B)	Excellent(5)		
Maintenance of course diary (A)	Excellent(5)		
Assessment of Outcomes (A)	Excellent(5)		
Faculty evaluation amp remarks of the HoD (A)	Excellent(5)		
Conduct of Seminar, Mini project, Project amp Thesis (A)	Excellent(5)		
Computing facility in the department (A)	Excellent(5)		
Facility for co-curricular and extracurricular activities (B)	Fair(3)		
Students attending MOOC or other online courses (B)	Excellent(5)		

M.Tech

No of M.Tech Students	21

CIVIL ENGINEERING(STRUCTURAL ENGINEERING AND CONSTRUCTION MANAGEMENT)-(Full Time)

Key Aspects	Rating	Auditor Remarks	Principal's Response
Class/course committee meetings and action taken report (B)	Excellent(5)		
Syllabus coverage as per course plan (B)	Excellent(5)		
Evaluation of Answer Scripts (A):	Excellent(5)		
Maintenance of course diary (A)	Excellent(5)		
Faculty evaluation amp remarks of the HoD (A)	Excellent(5)		
Facility in the department library for references (C)	Excellent(5)		

Conduct of Seminar, Mini project, Project amp Thesis (A)	Excellent(5)		
Faculty with Ph.D for the program (A)	Excellent(5)		
Computing facility in the department (A)	Excellent(5)		
Facility for co-curricular and extracurricular activities (B)	Fair(3)		
Students attending MOOC or other online courses (B)	Excellent(5)		

ELECTRONICS AND COMMUNICATION ENGINEERING(CONTROL AND INSTRUMENTATION)-(Full Time)

Key Aspects	Rating	Auditor Remarks	Principal's Response
Class/course committee meetings and action taken report (B)	Not Relevant		
Syllabus coverage as per course plan (B)	Not Relevant		
Evaluation of Answer Scripts (A):	Not Relevant		
Maintenance of course diary (A)	Not Relevant		
Faculty evaluation amp remarks of the HoD (A)	Not Relevant		
Facility in the department library for references (C)	Not Relevant		
Conduct of Seminar, Mini project, Project amp Thesis (A)	Not Relevant		
Faculty with Ph.D for the program (A)	Not Relevant		
Computing facility in the department (A)	Not Relevant		
Facility for co-curricular and extracurricular activities (B)	Not Relevant		

Students attending MOOC or other online courses (B)	Not Relevant		

COMPUTER SCIENCE AND ENGINEERING(COMPUTER SCIENCE AND ENGINEERING)-(Full Time)

Key Aspects	Rating	Auditor Remarks	Principal's Response
Class/course committee meetings and action taken report (B)	Excellent(5)		
Syllabus coverage as per course plan (B)	Excellent(5)		
Evaluation of Answer Scripts (A):	Excellent(5)		
Maintenance of course diary (A)	Excellent(5)		
Faculty evaluation amp remarks of the HoD (A)	Excellent(5)		
Facility in the department library for references (C)	Excellent(5)		
Conduct of Seminar, Mini project, Project amp Thesis (A)	Excellent(5)		
Faculty with Ph.D for the program (A)	Excellent(5)		
Computing facility in the department (A)	Excellent(5)		
Facility for co-curricular and extracurricular activities (B)	Fair(3)		
Students attending MOOC or other online courses (B)	Excellent(5)		

ELECTRICAL AND ELECTRONICS ENGINEERING(POWER ELECTRONICS)-(Full Time)

Key Aspects	Rating	Auditor Remarks	Principal's Response
Class/course committee meetings and action taken report (B)	Excellent(5)		
Syllabus coverage as per course plan (B)	Excellent(5)		

Evaluation of Answer Scripts (A):	Excellent(5)		
Maintenance of course diary (A)	Excellent(5)		
Faculty evaluation amp remarks of the HoD (A)	Excellent(5)		
Facility in the department library for references (C)	Excellent(5)		
Conduct of Seminar, Mini project, Project amp Thesis (A)	Excellent(5)		
Faculty with Ph.D for the program (A)	Excellent(5)		
Computing facility in the department (A)	Excellent(5)		
Facility for co-curricular and extracurricular activities (B)	Fair(3)		
Students attending MOOC or other online courses (B)	Excellent(5)		

ELECTRONICS AND COMMUNICATION ENGINEERING(COMMUNICATION ENGINEERING AND SIGNAL PROCESSING)-(Full Time)

Key Aspects	Rating	Auditor Remarks	Principal's Response
Class/course committee meetings and action taken report (B)	Not Relevant		
Syllabus coverage as per course plan (B)	Not Relevant		
Evaluation of Answer Scripts (A):	Not Relevant		
Maintenance of course diary (A)	Not Relevant		
Faculty evaluation amp remarks of the HoD (A)	Not Relevant		
Facility in the department library for references (C)	Not Relevant		

Conduct of Seminar, Mini project, Project amp Thesis (A)	Not Relevant		
Faculty with Ph.D for the program (A)	Not Relevant		
Computing facility in the department (A)	Not Relevant		
Facility for co-curricular and extracurricular activities (B)	Not Relevant		
Students attending MOOC or other online courses (B)	Not Relevant		

MECHANICAL ENGINEERING(THERMAL ENGINEERING)-(Full Time)

Key Aspects	Rating	Auditor Remarks	Principal's Response
Class/course committee meetings and action taken report (B)	Not Relevant		
Syllabus coverage as per course plan (B)	Not Relevant		
Evaluation of Answer Scripts (A):	Not Relevant		
Maintenance of course diary (A)	Not Relevant		
Faculty evaluation amp remarks of the HoD (A)	Not Relevant		
Facility in the department library for references (C)	Not Relevant		
Conduct of Seminar, Mini project, Project amp Thesis (A)	Not Relevant		
Faculty with Ph.D for the program (A)	Not Relevant		
Computing facility in the department (A)	Not Relevant		
Facility for co-curricular and extracurricular activities (B)	Not Relevant		

Students attending MOOC or other online courses (B)	Not Relevant		

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Ph: 0460 - 2212240, Fax:04602213513
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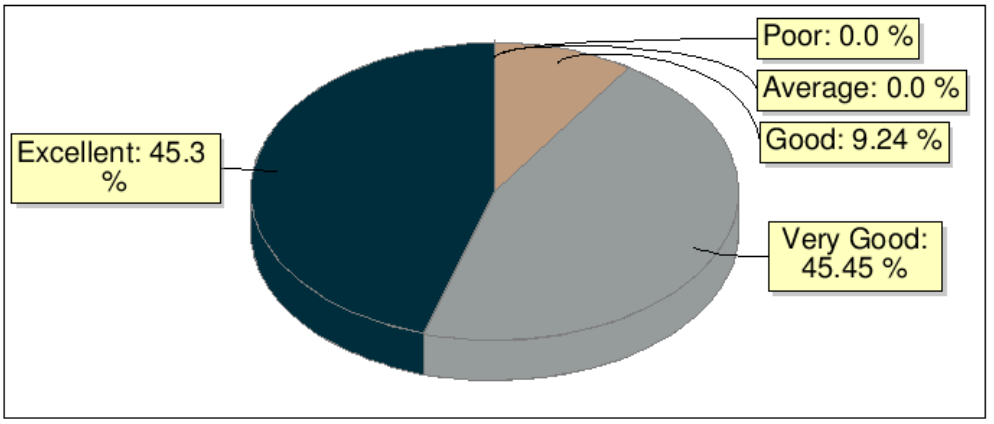
Course Wise 2021-22

Batch : B.Tech , 2019-2023
Staff Name : Ms Namitha P
Subject Code : CST304
Subject Name : COMPUTER GRAPHICS AND IMAGE PROCESSING
No. of responses : 44 / 59 (74.58 %)

Department : Computer Science and Engineering
Semester 6
Course-wise Feedback Number : 1
Date : 28 May 2022

No	Questions	Score on a scale of 5					Feedback Percentage	Average Score (5)
		1	2	3	4	5		
	Subject Command							
1	Subject Knowledge of the faculty	0	0	4	20	20	87.3	4.4
	Presentation Skills							
1	Logical sequencing of lectures in online meeting platform	0	0	5	20	19	86.4	4.3
2	Communication skill in English	0	0	6	19	19	85.9	4.3
	Use of Teaching Aid							
1	Usage of online resources/LMS/Google classroom	0	0	4	20	20	87.3	4.4
2	Provision of class notes online	0	0	5	21	18	85.9	4.3
	Class Control/Management							
1	Encourage the students to ask questions.	0	0	3	20	21	88.2	4.4
2	Impartiality in dealing with students	0	0	3	21	20	87.7	4.4
3	Maintenance of discipline in the class	0	0	5	18	21	87.3	4.4
4	Creation of an environment conducive to learning	0	0	4	21	19	86.8	4.3
5	Encouragement for critical thinking	0	0	3	21	20	87.7	4.4
	Time Sense							
1	Completion of syllabus within time	0	0	4	20	20	87.3	4.4
2	Punctuality in running the class and in returning test papers/assignments	0	0	4	21	19	86.8	4.3
	Helping Attitude							
1	Provision of helpful and constructive feedback	0	0	4	20	20	87.3	4.4
2	Value addition in terms of project ideas and information beyond syllabus	0	0	3	20	21	88.2	4.4
3	Concern for student progress and needs	0	0	4	18	22	88.2	4.4
	Total Count	**0**	**0**	**61**	**300**	**299**	**87.2**	**4.36**


Poor: 0.0 %
Average: 0.0 %
Good: 9.24 %
Excellent: 45.3 %
Very Good: 45.45 %